

FIG. 1

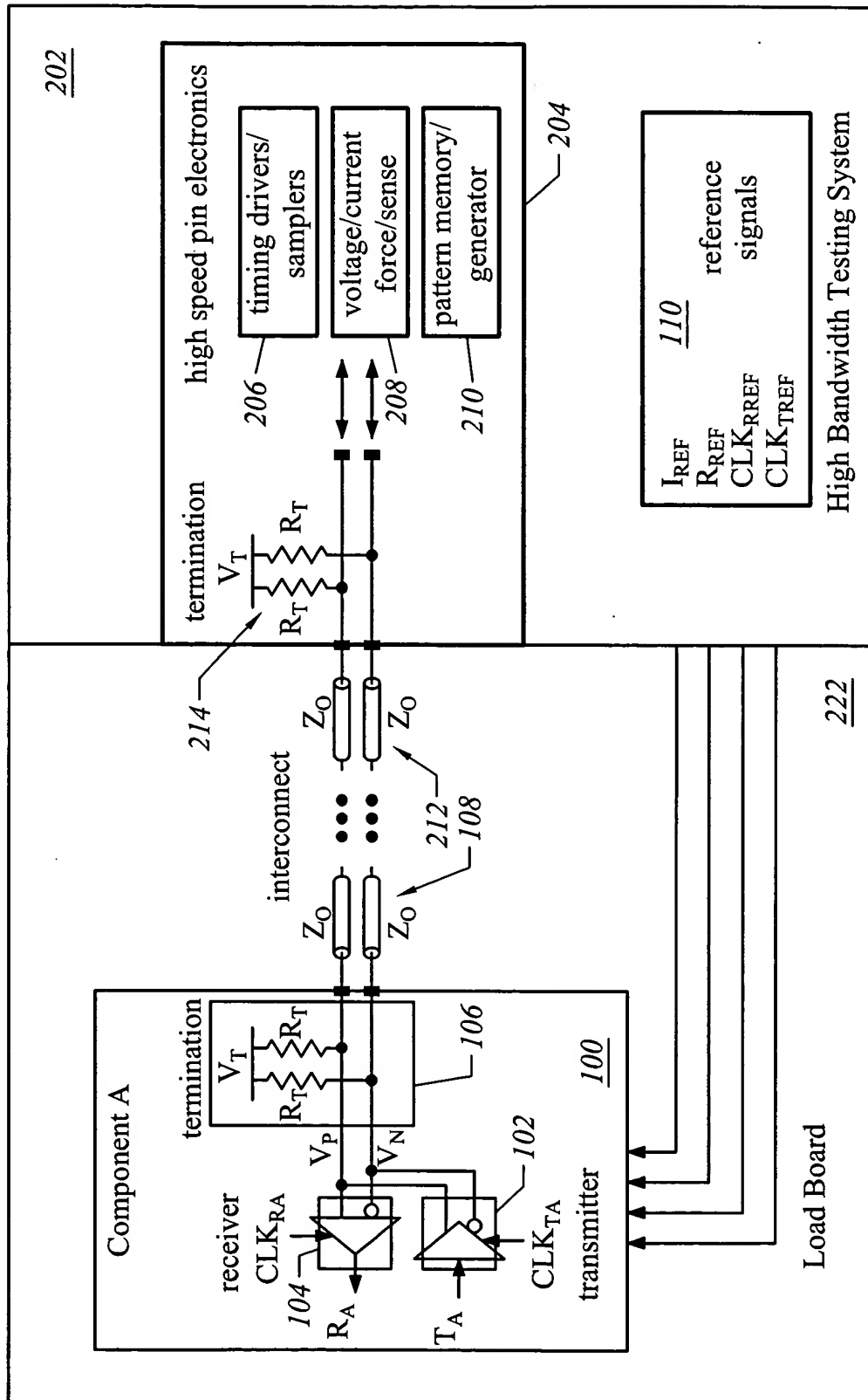


FIG. 2

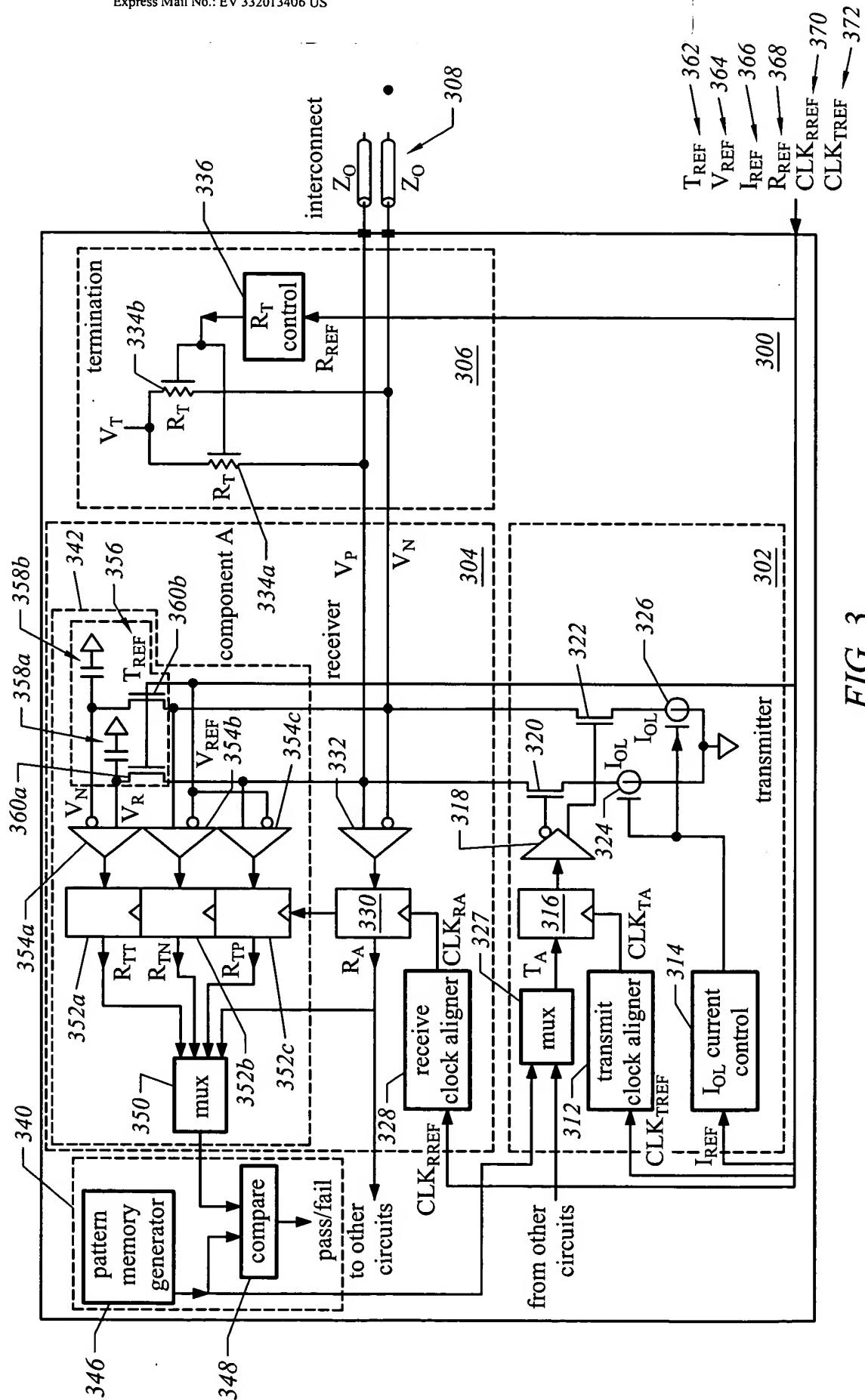
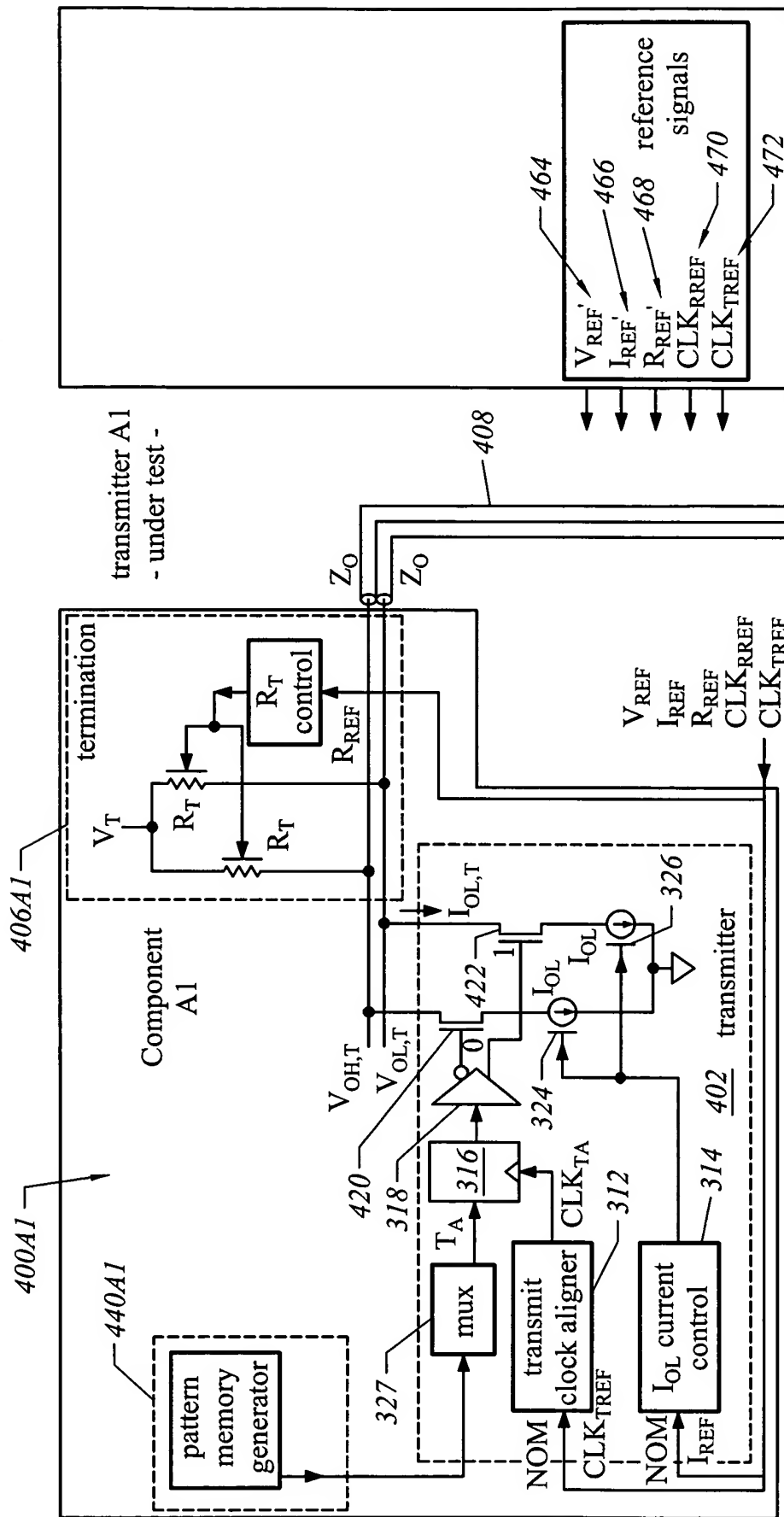


FIG. 3



(See Fig. 4B)

FIG. 4A

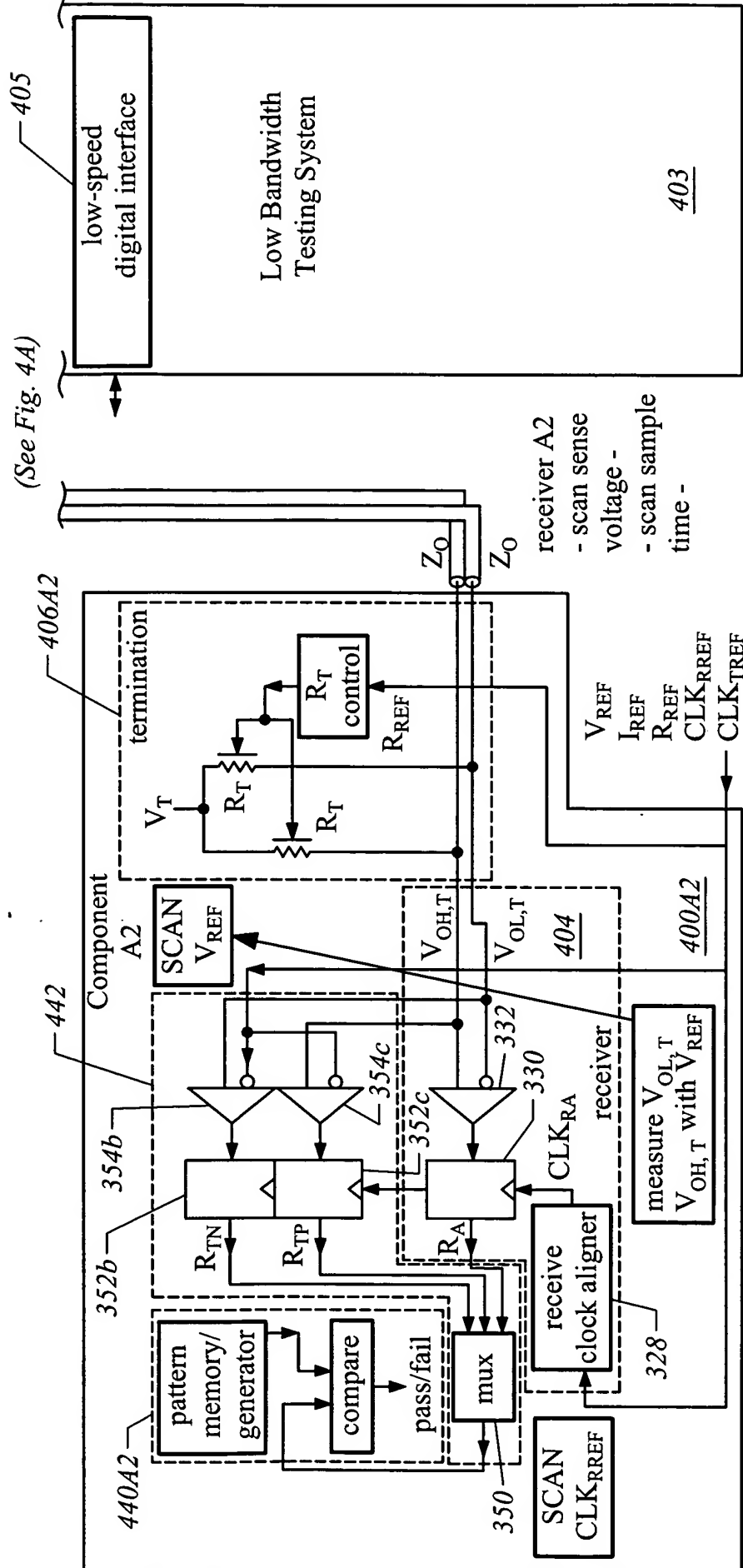
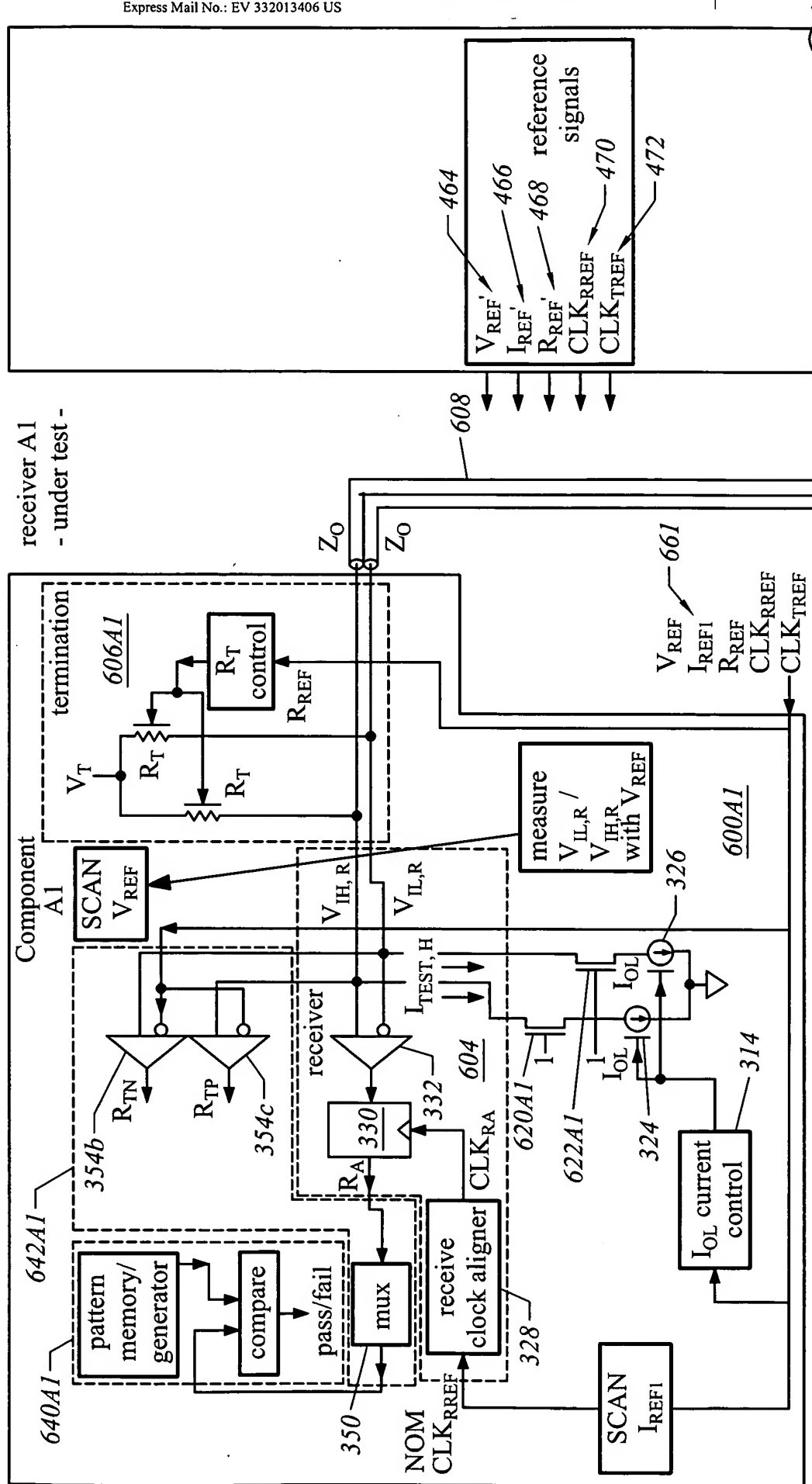




FIG. 5



(See Fig. 6B)

FIG. 6A

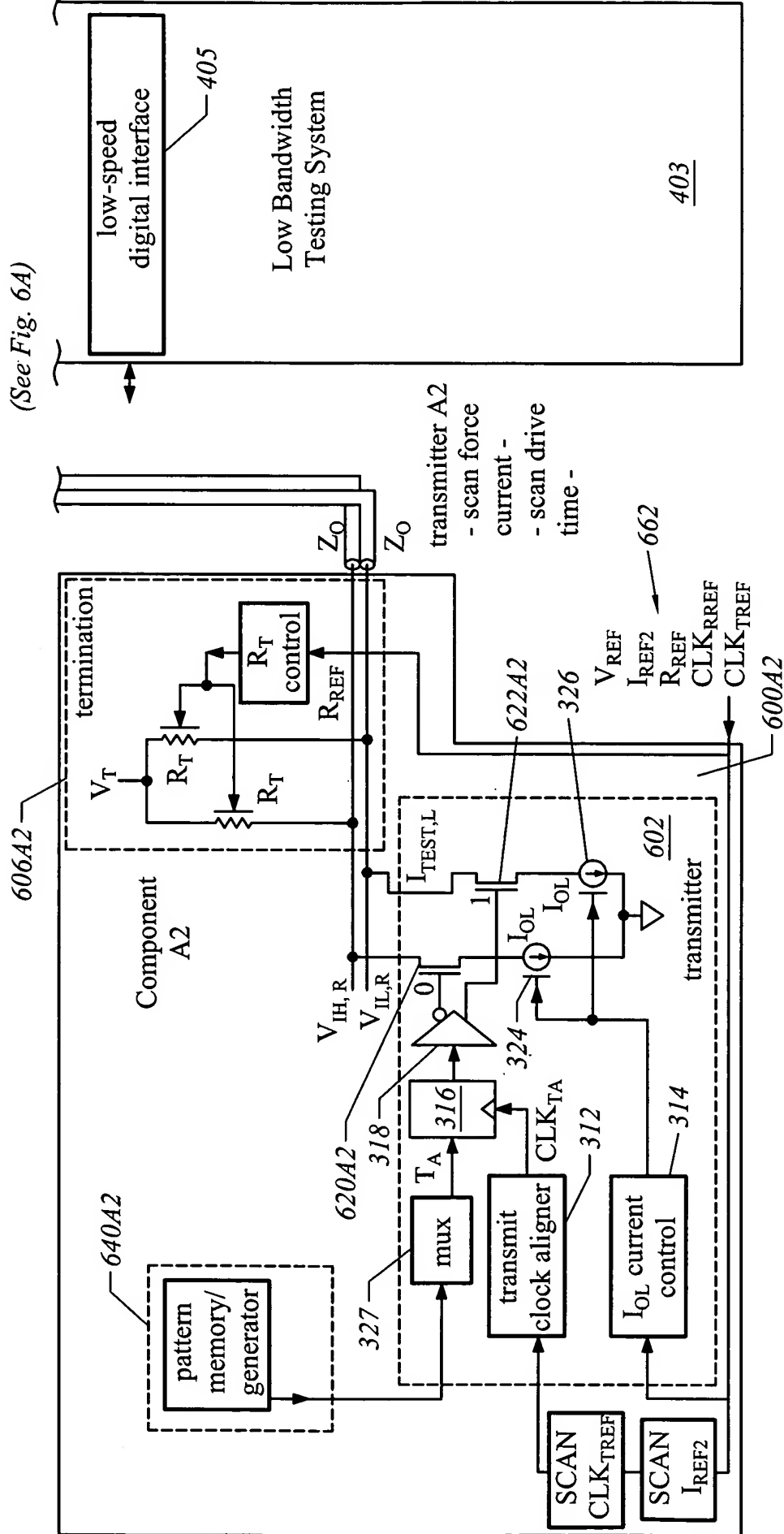


FIG. 6B





**FIG. 7**



(See Fig. 8B)

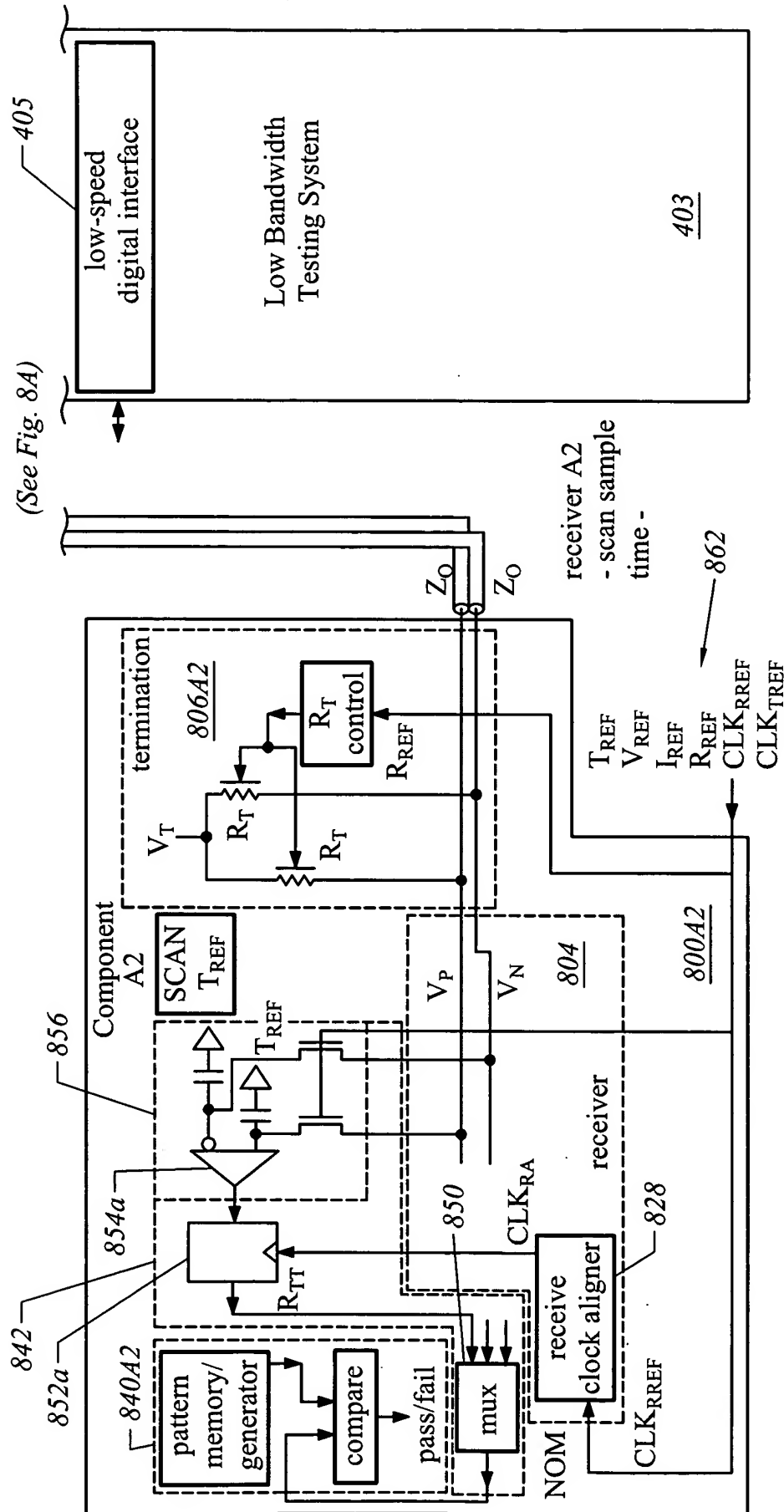
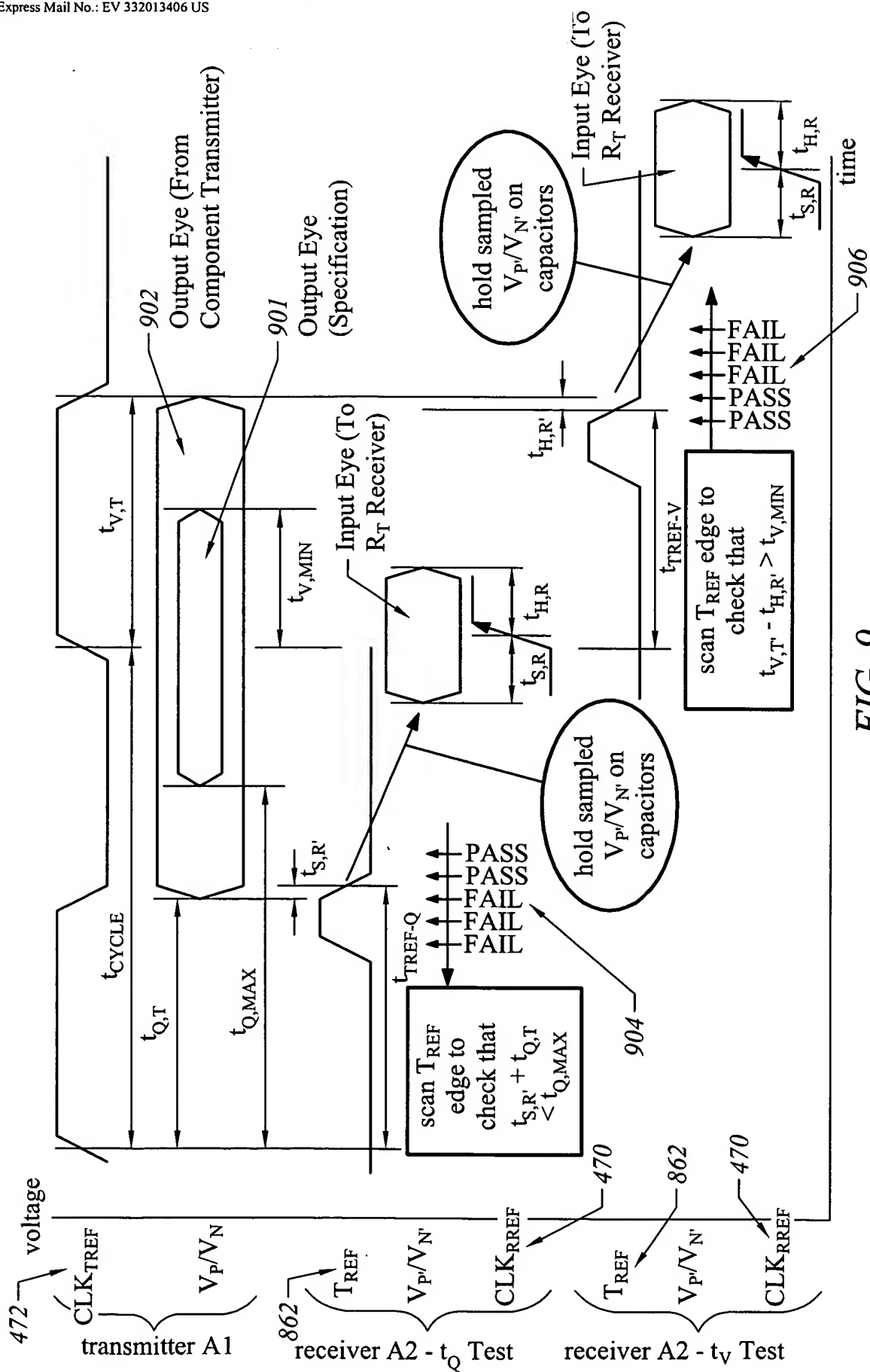


FIG. 8B



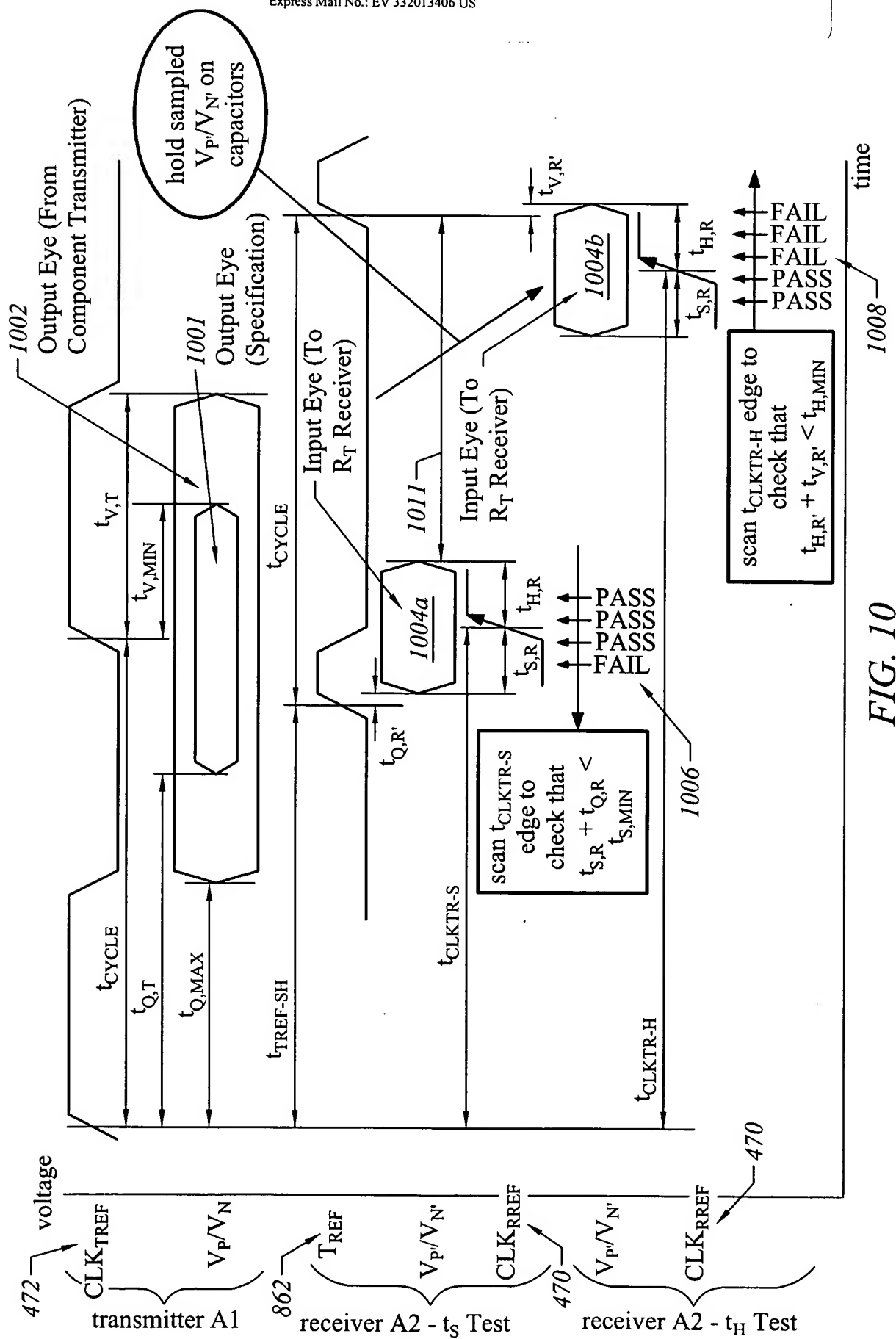


FIG. 10

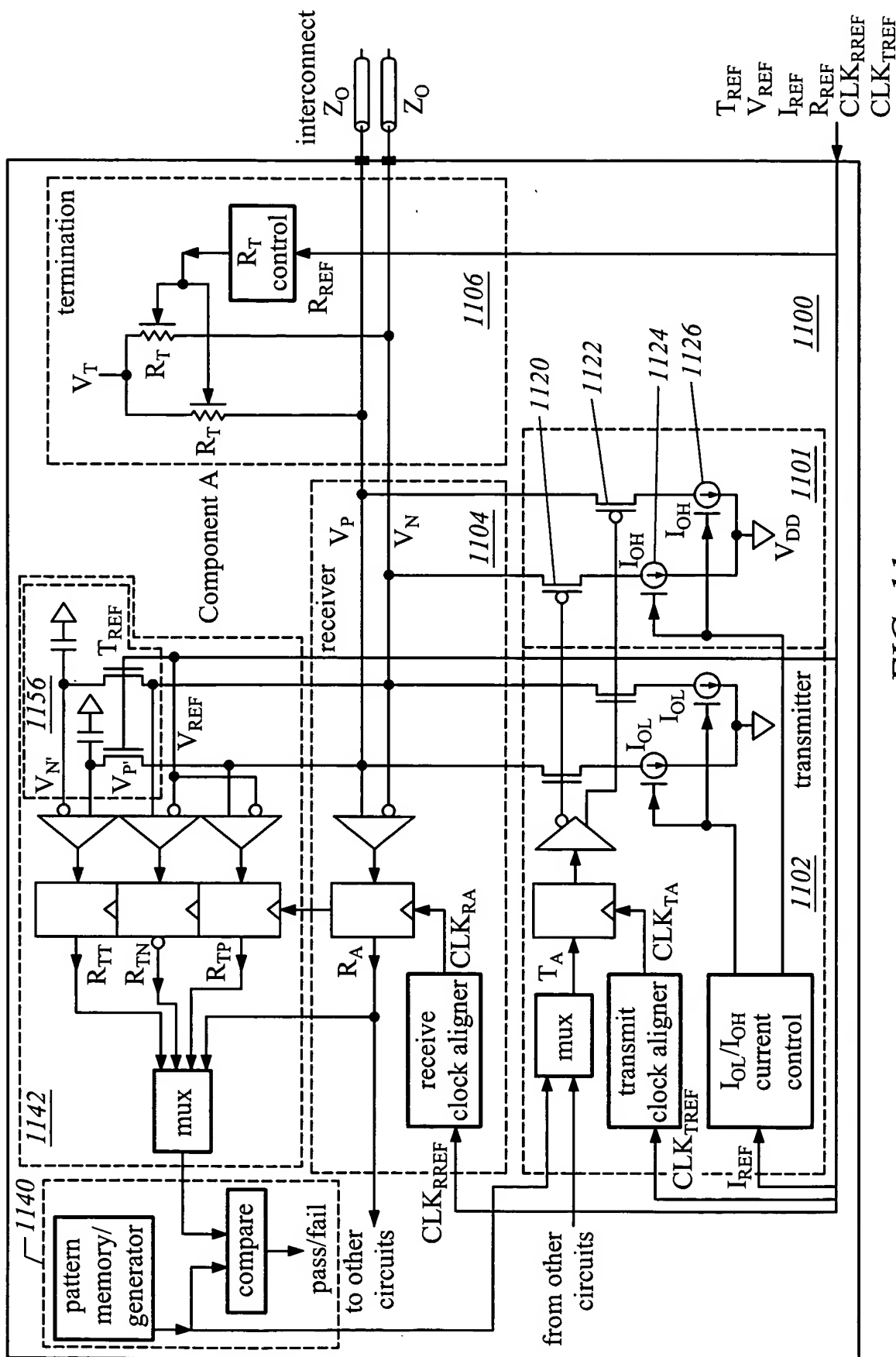


FIG. 11

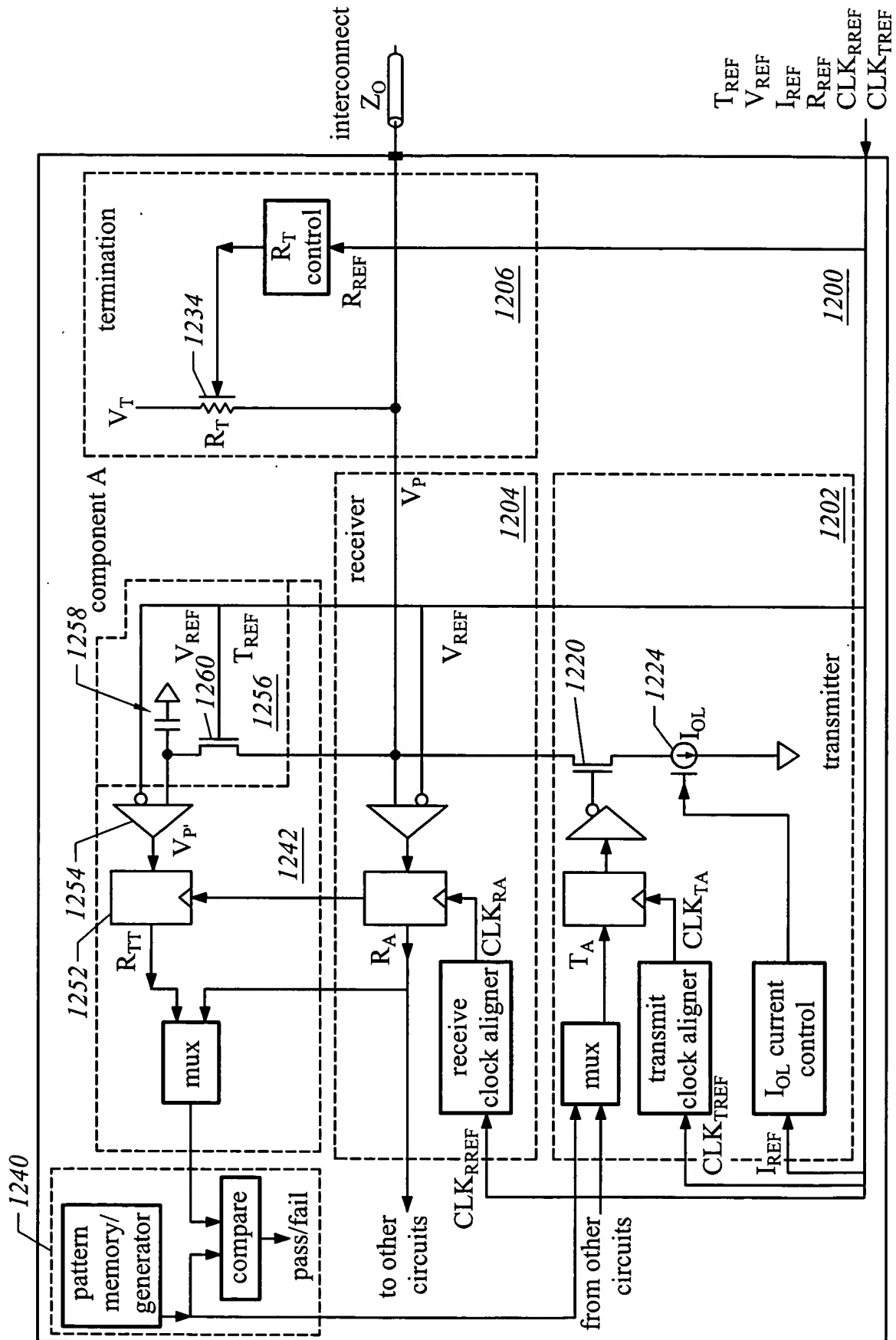


FIG. 12

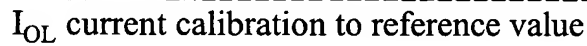


FIG. 13

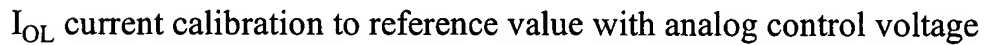


FIG. 14



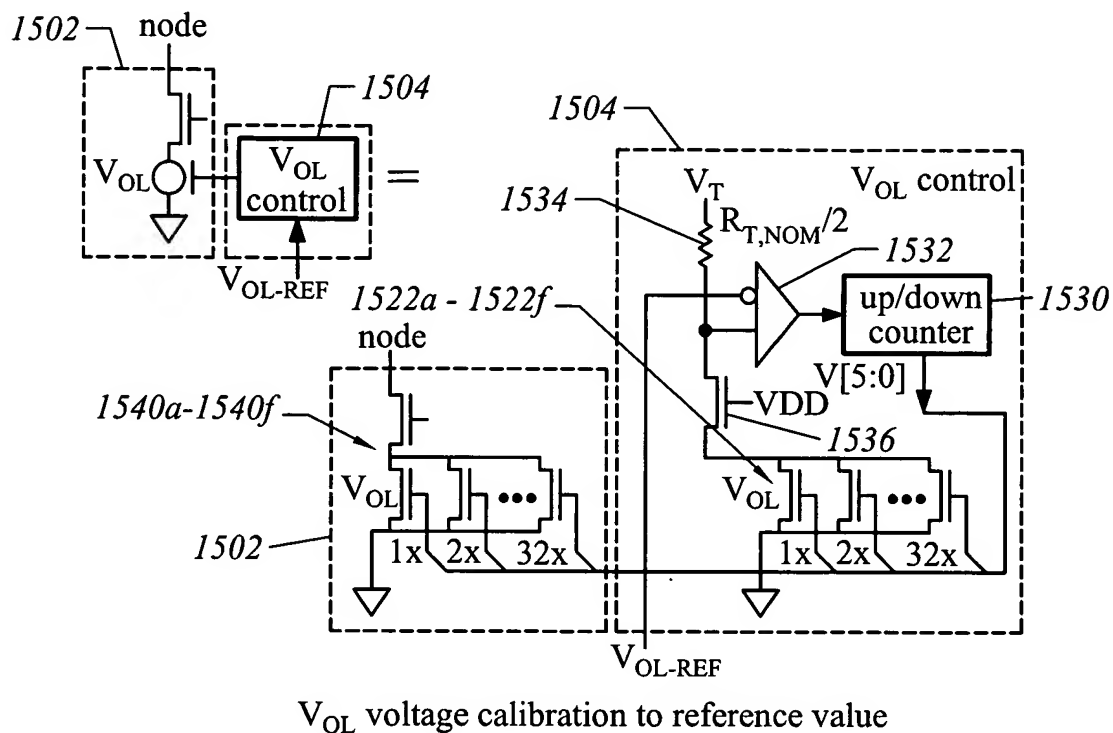


FIG. 15

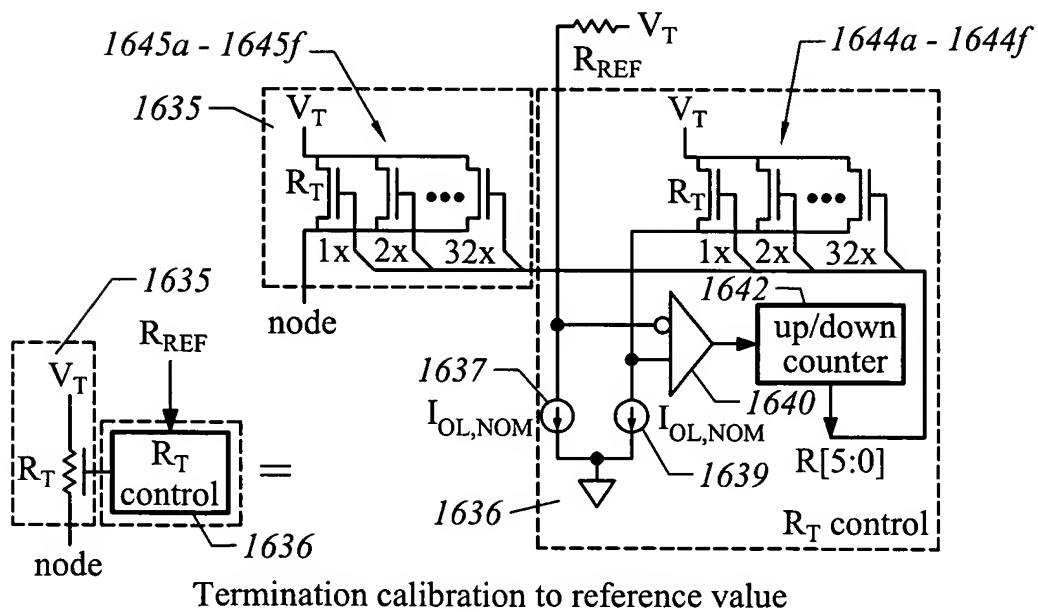


FIG. 16



1812

CLK<sub>XREF</sub>

phase detect 1802

filter 1804

voltage controlled delay line 1806

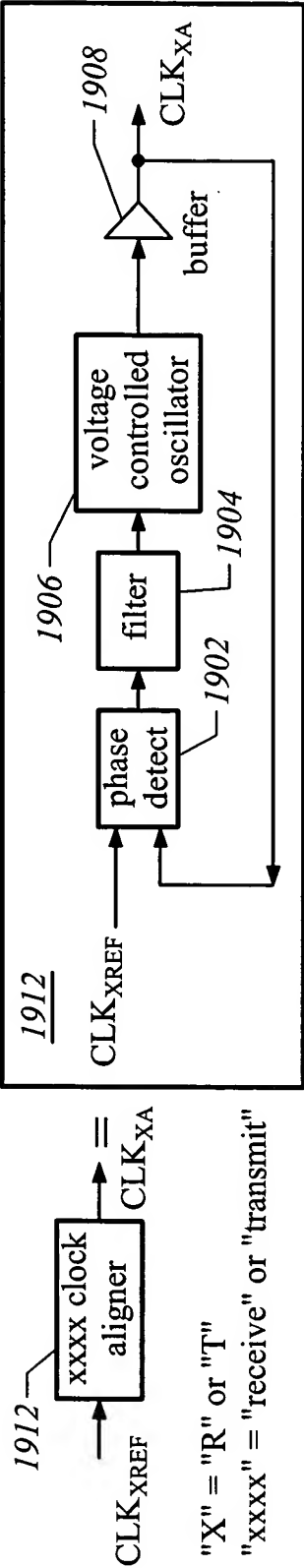
buffer 1808

CLK<sub>XA</sub>

$t_D$

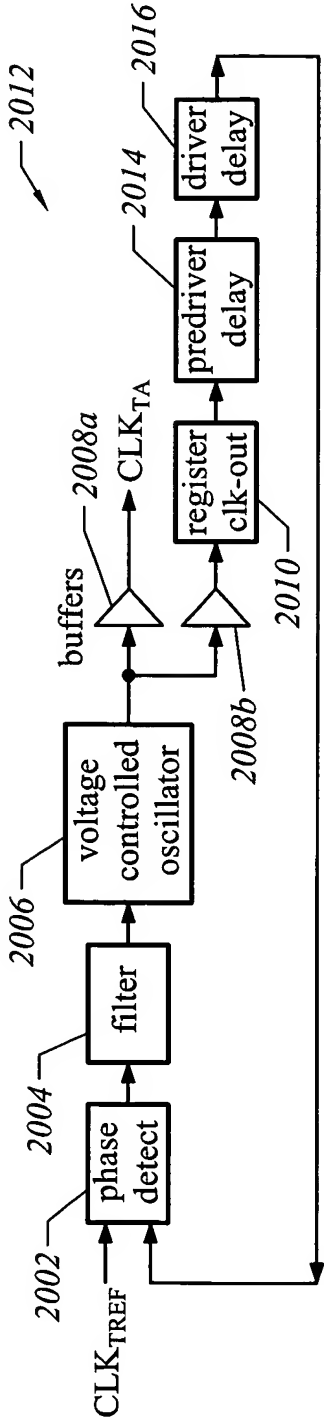
$t_B$

"xxxx" = "receive" or "transmit"



Receive or transmit clock alignment using PLL (phase-locked-loop)

FIG. 19



Transmitter clock aligner with output register/predriver/driver loop compensation

FIG. 20

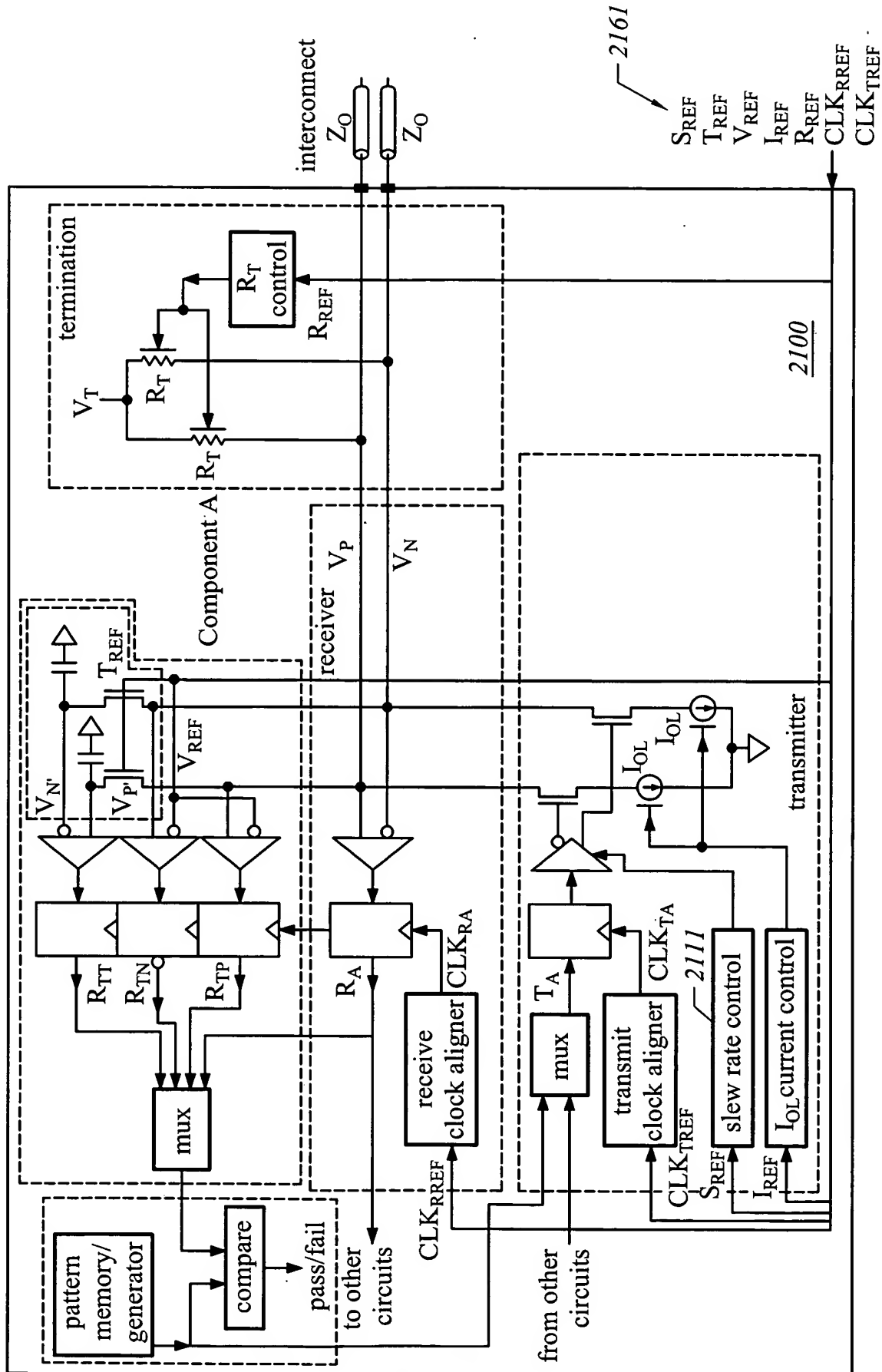
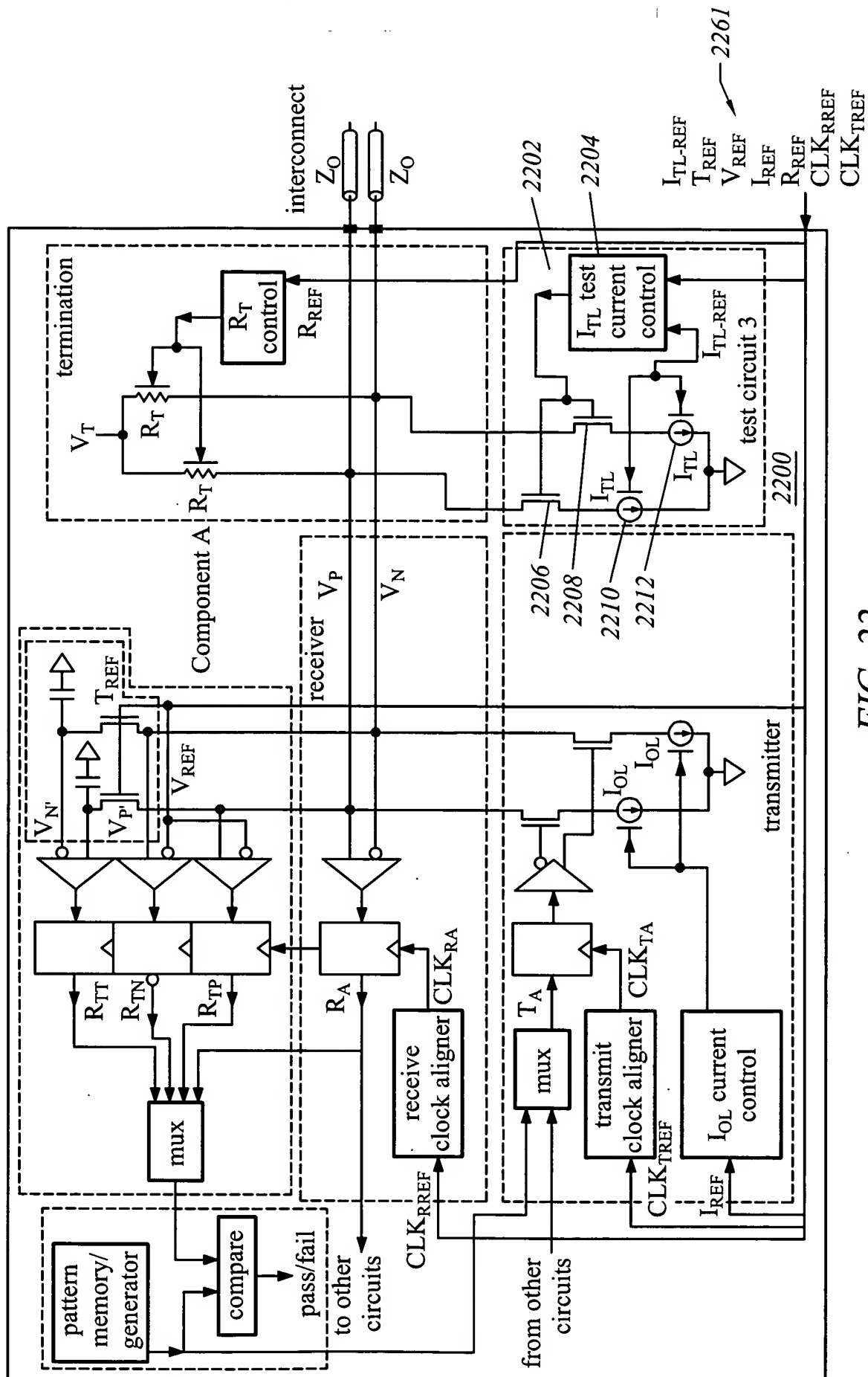
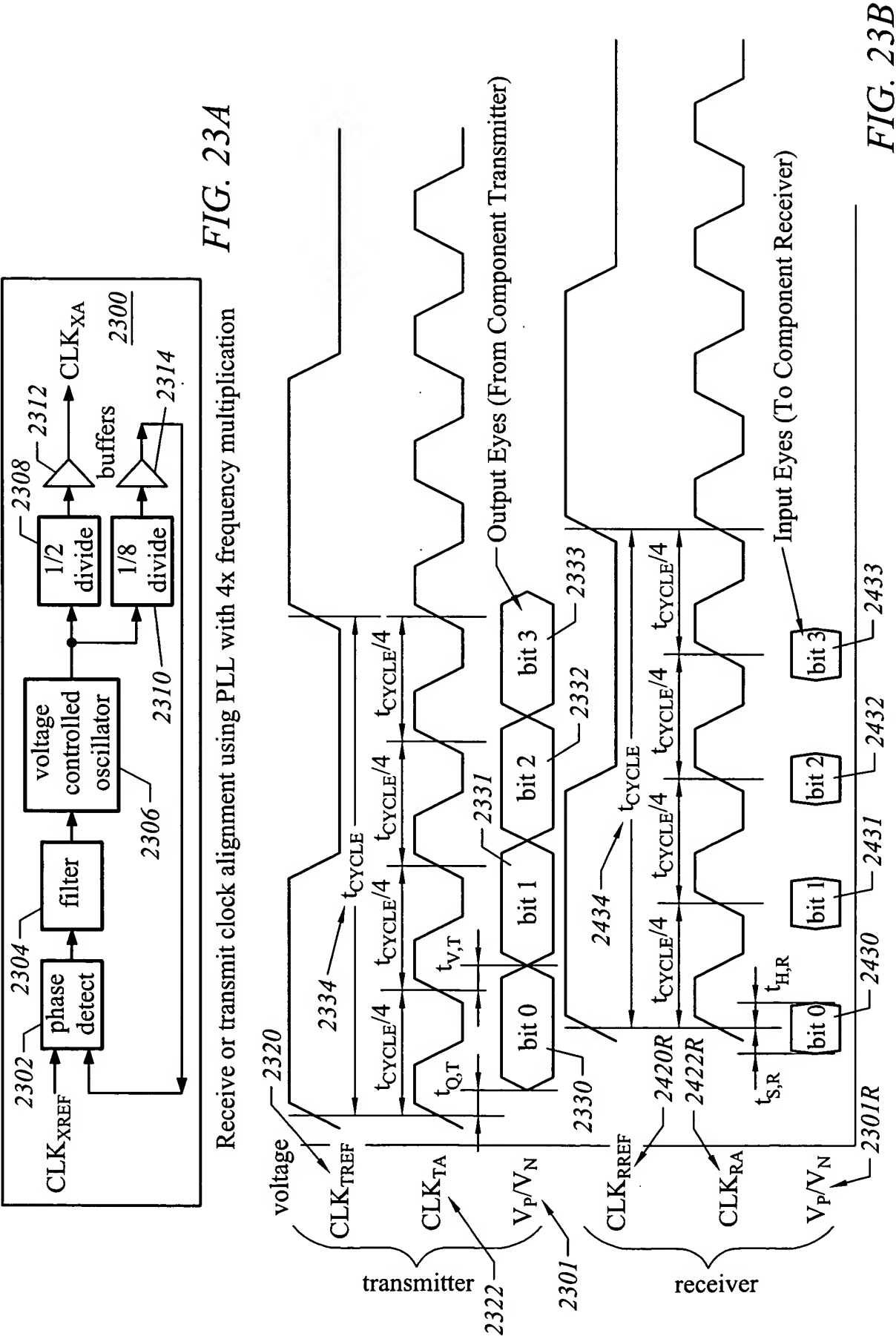


FIG. 21





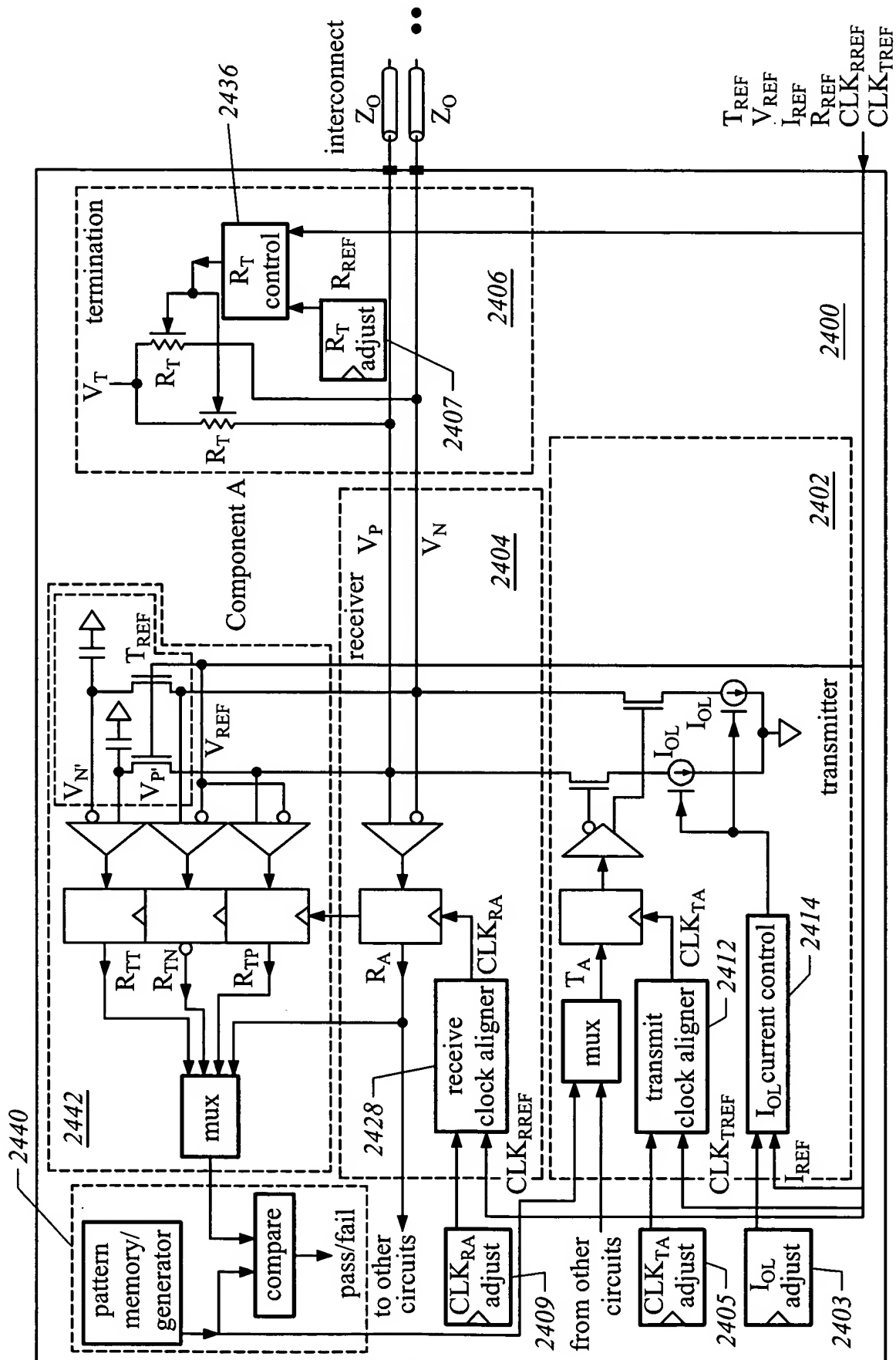


FIG. 24

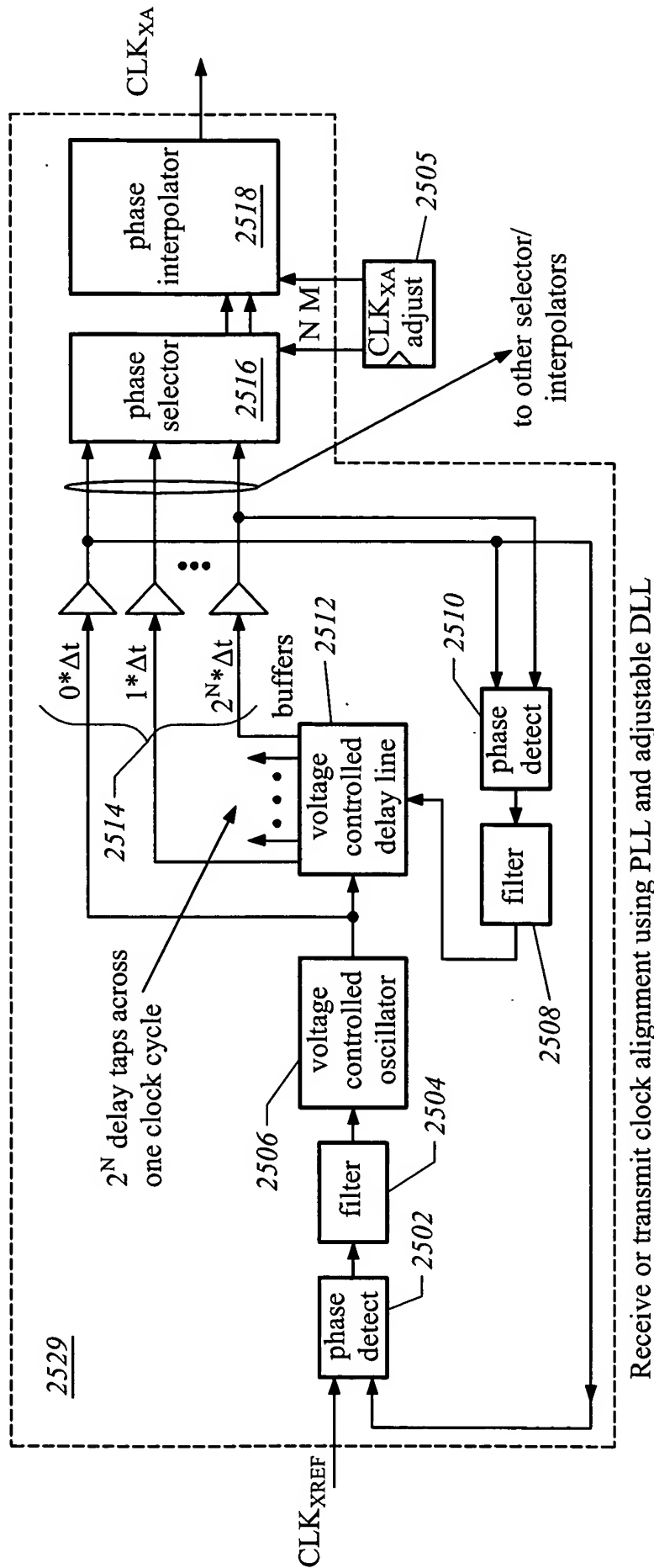
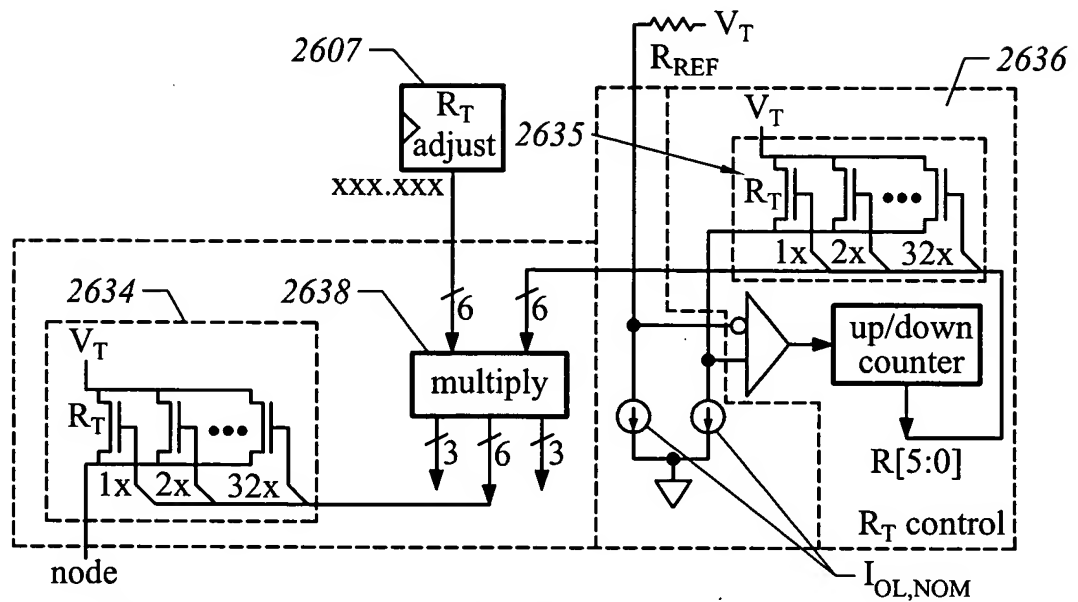


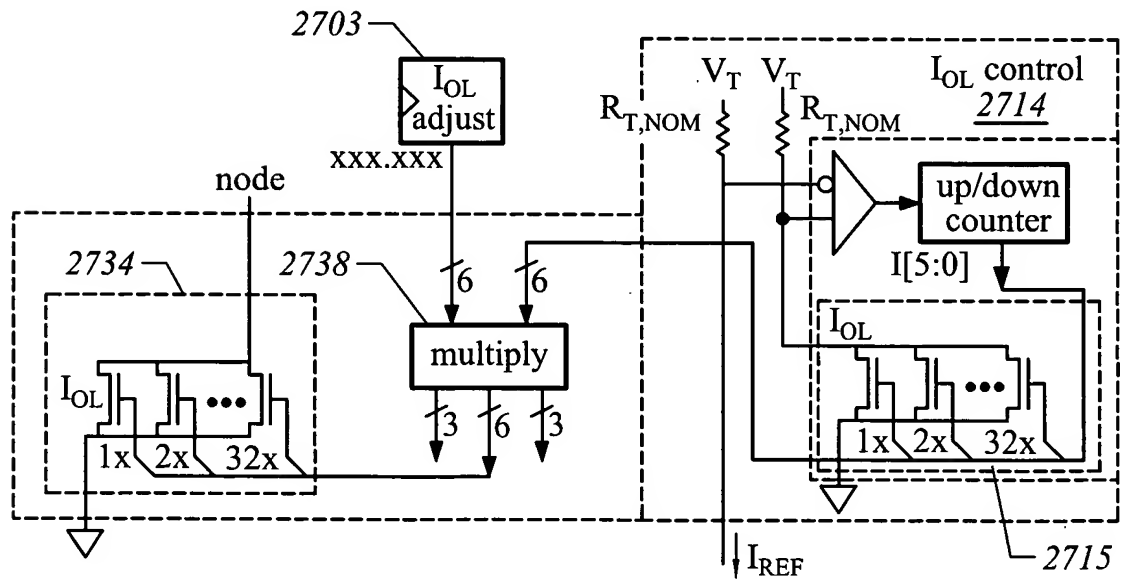
FIG. 25





Adjustable termination with calibration to reference value

FIG. 26



Adjustable  $I_{OL}$  current with calibration to reference value

FIG. 27

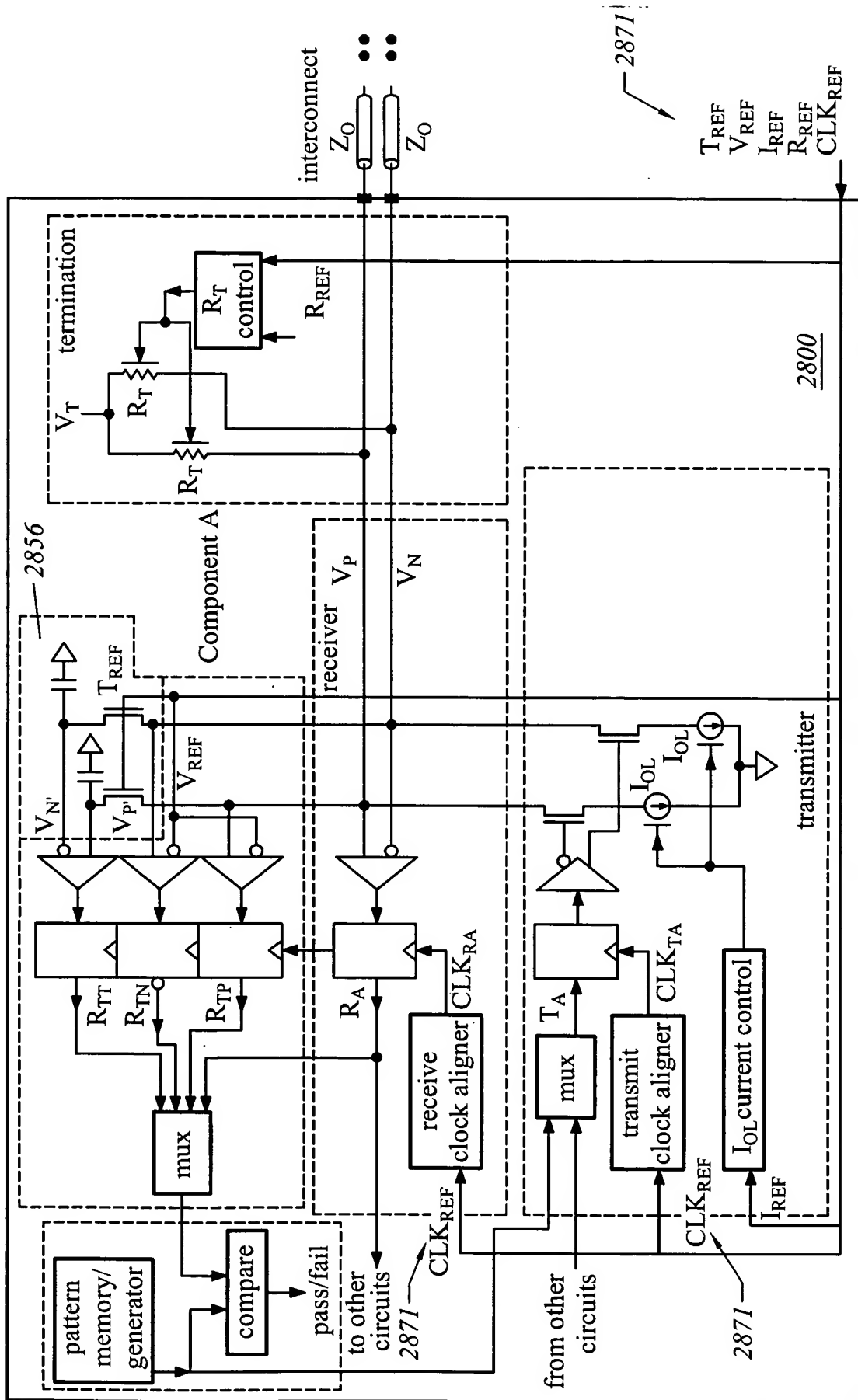


FIG. 28

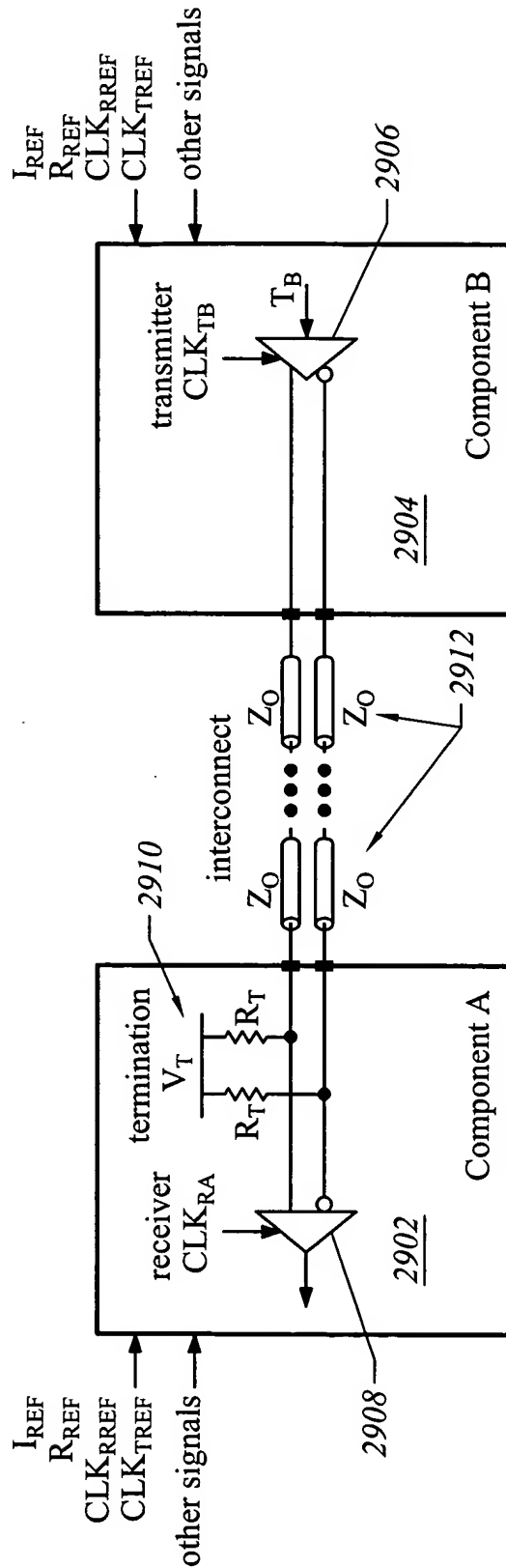
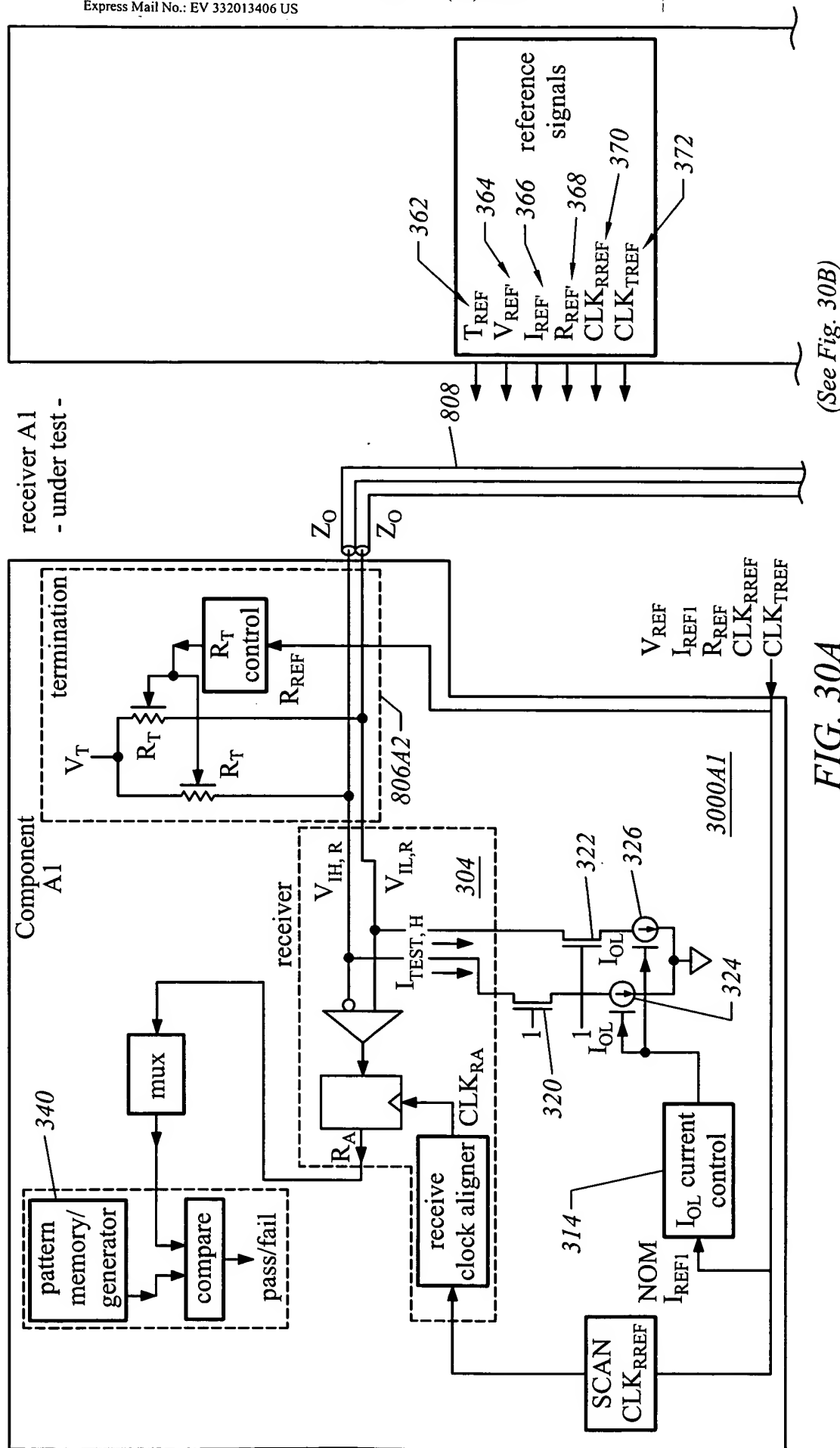
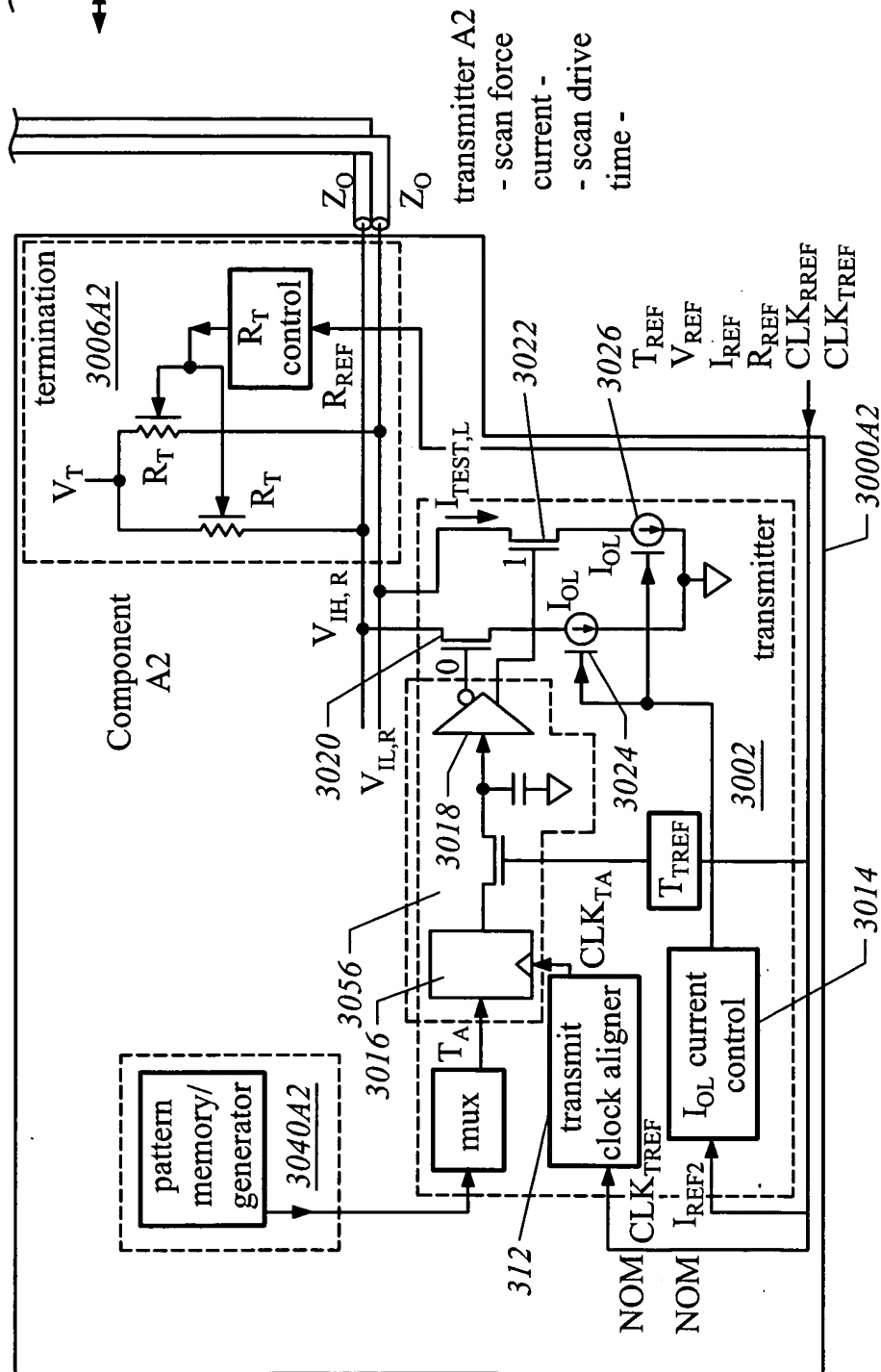


FIG. 29





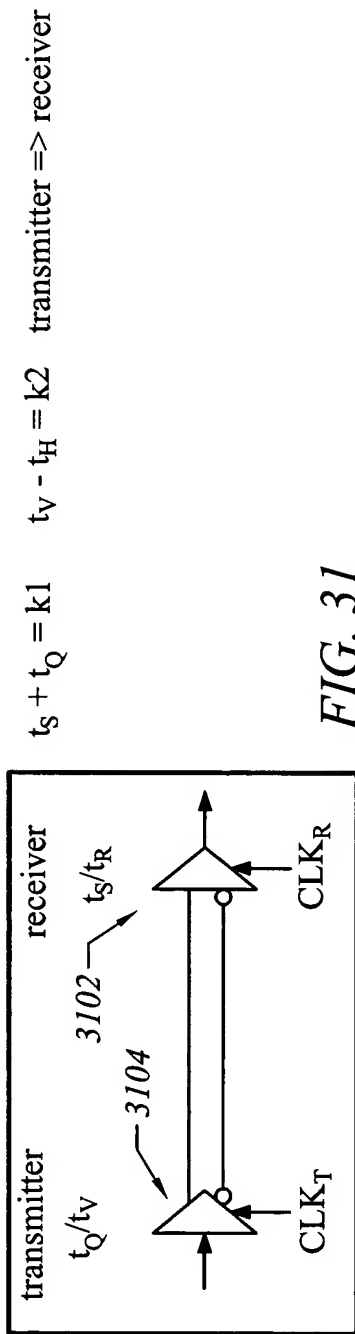


FIG. 31

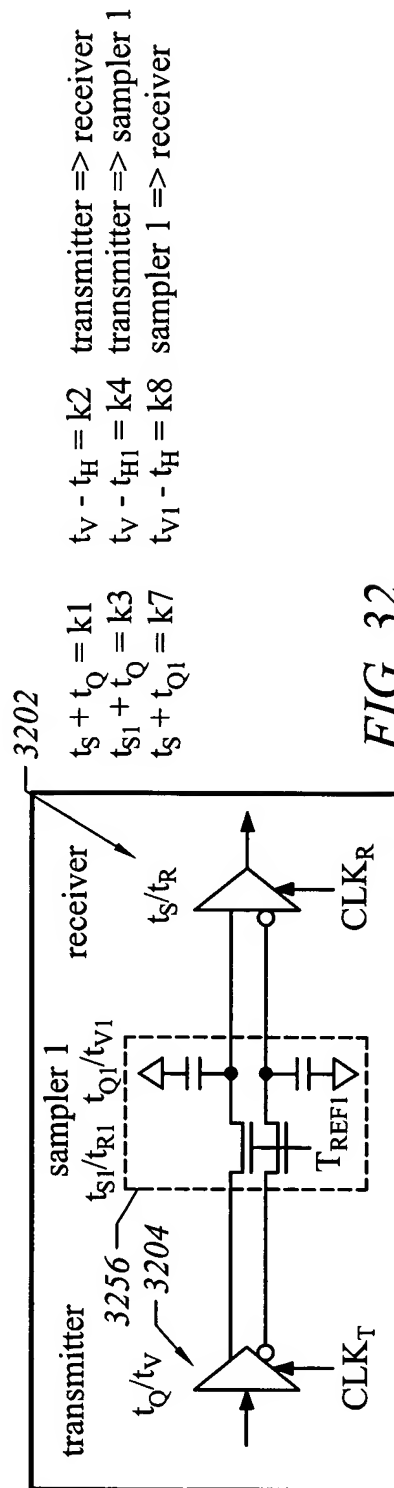


FIG. 32

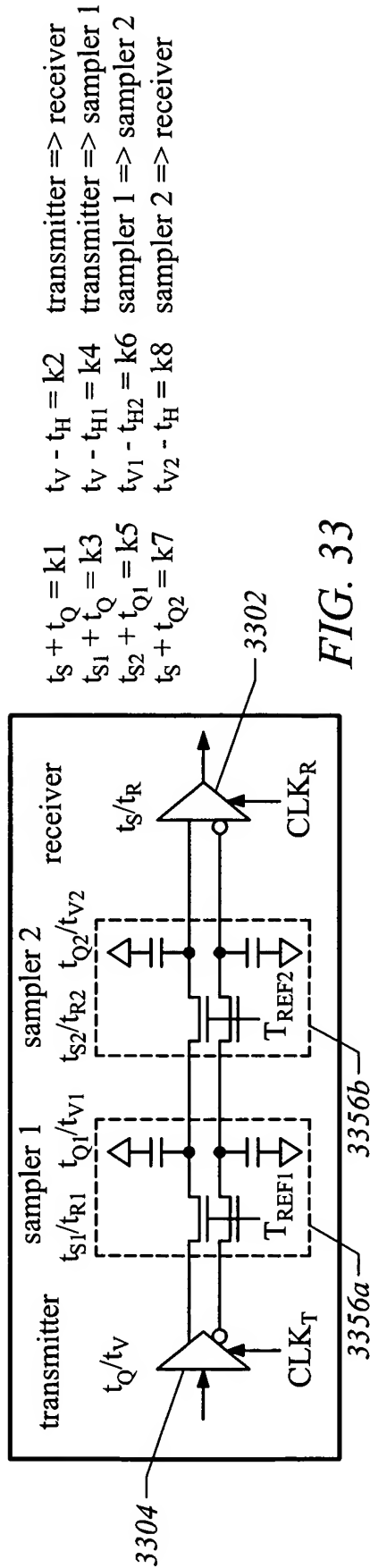


FIG. 33

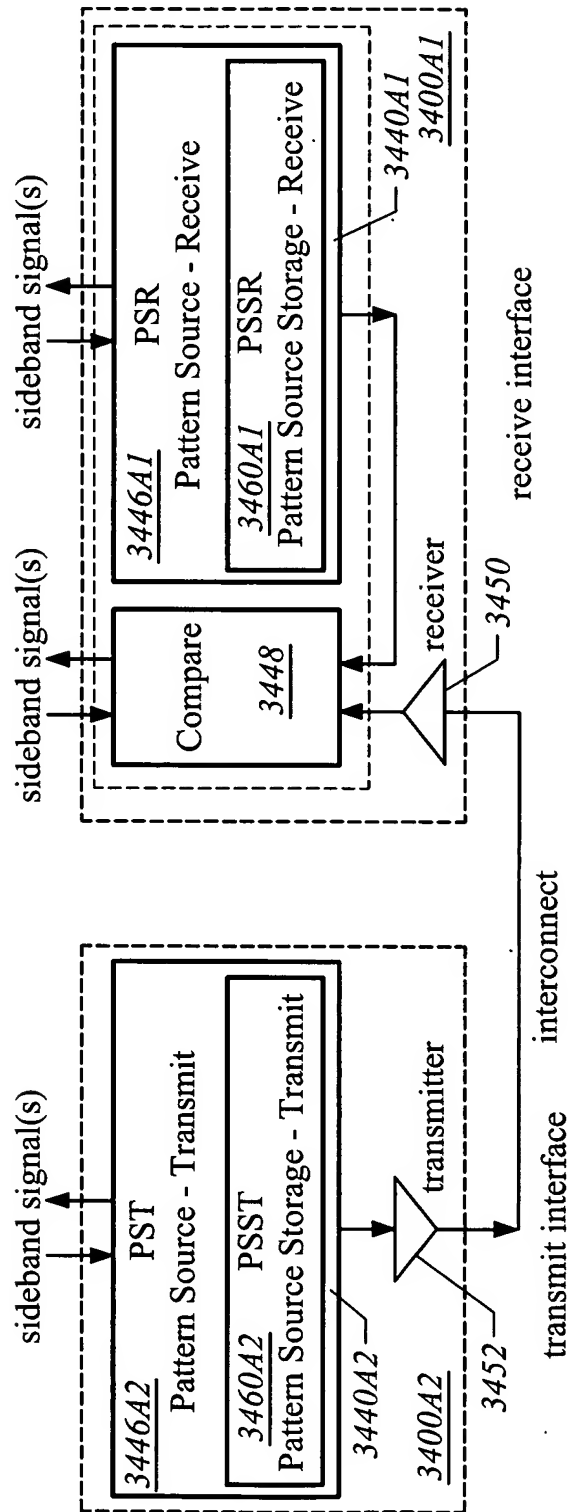


FIG. 34

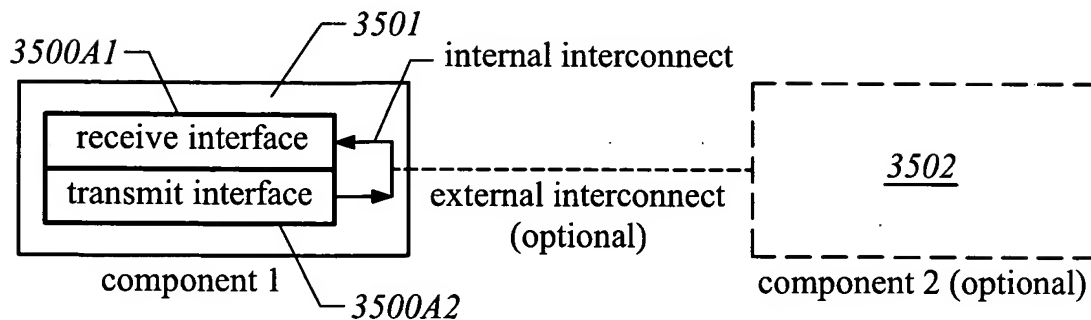


FIG. 35A

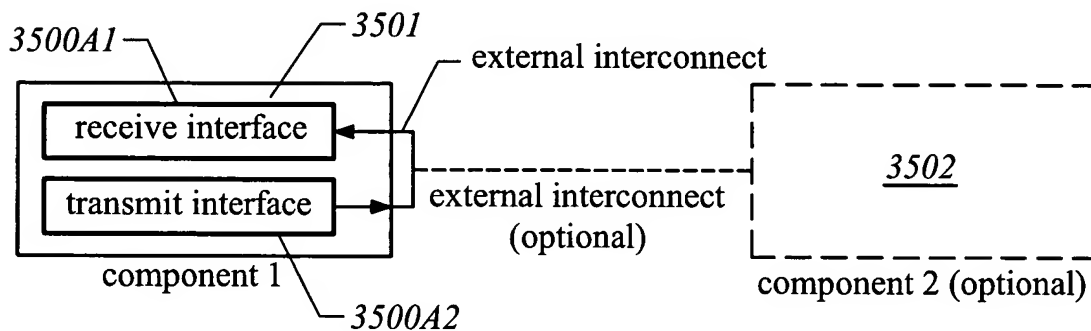


FIG. 35B

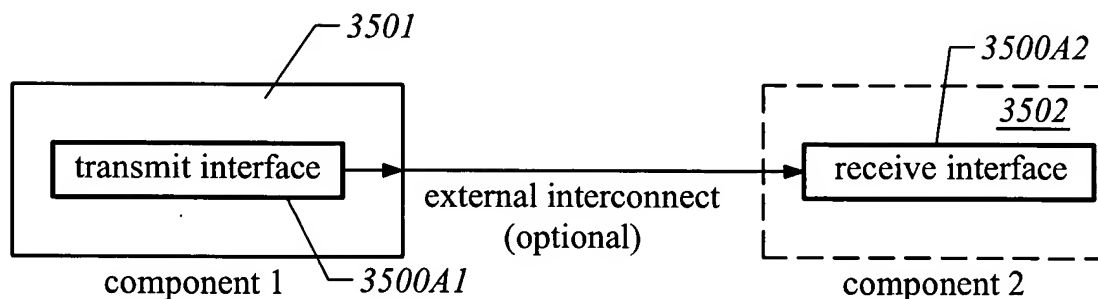


FIG. 35C



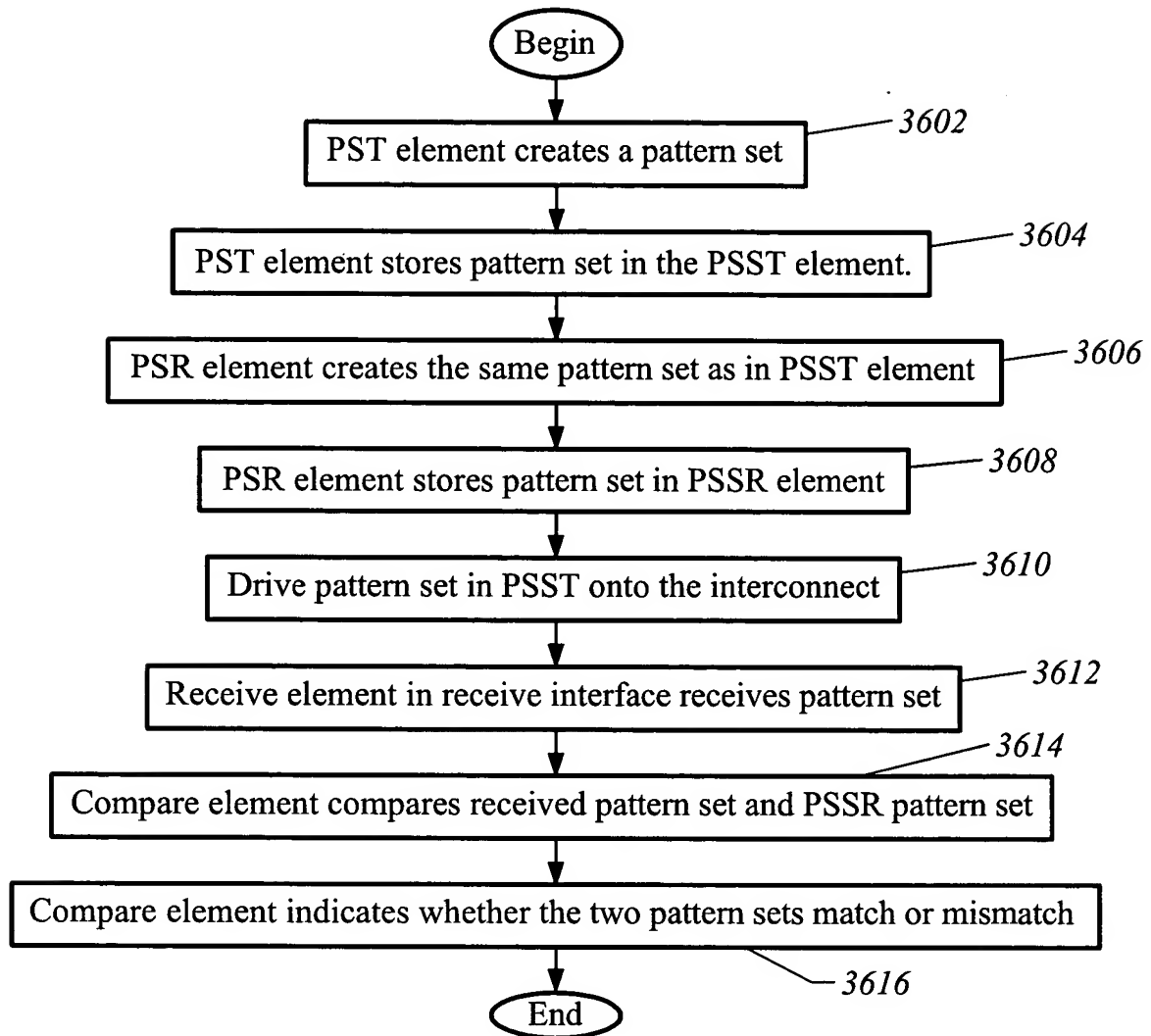


FIG. 36

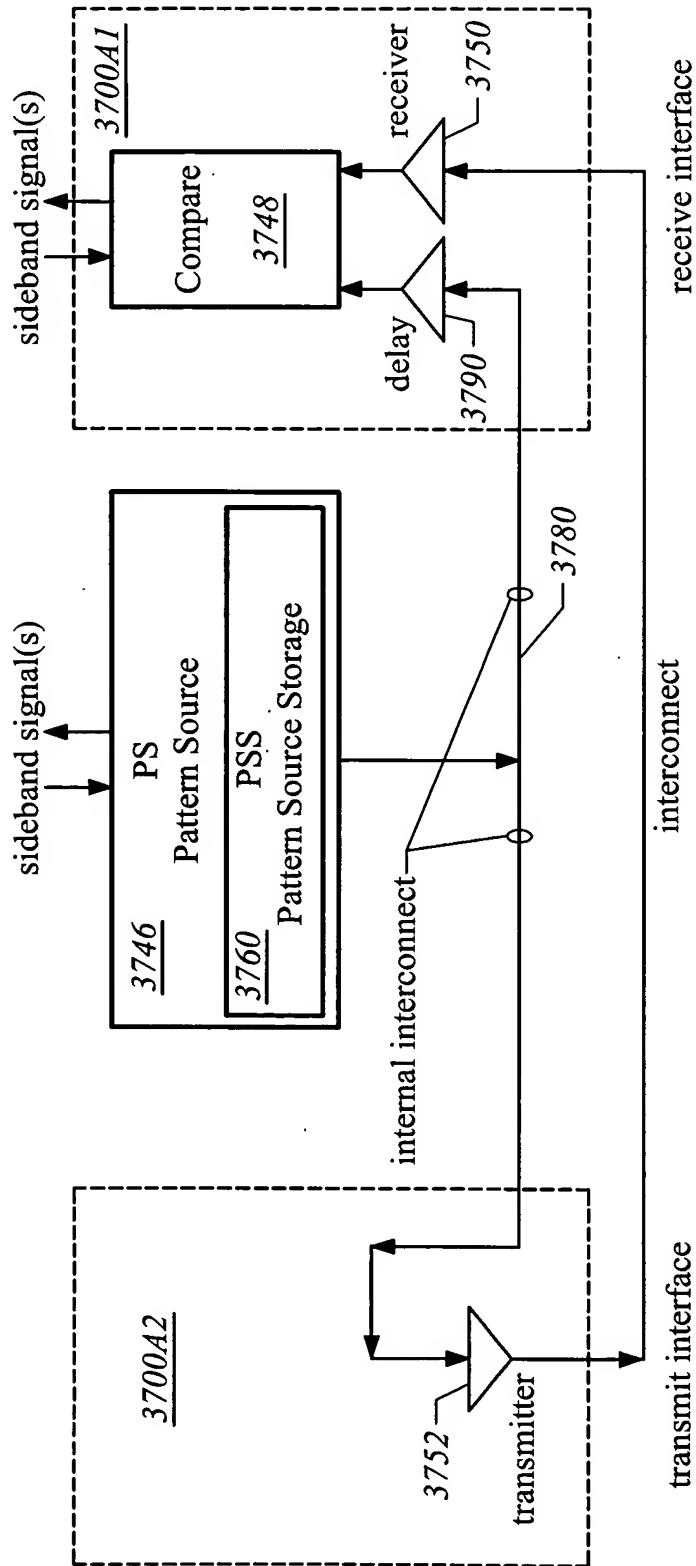


FIG. 37

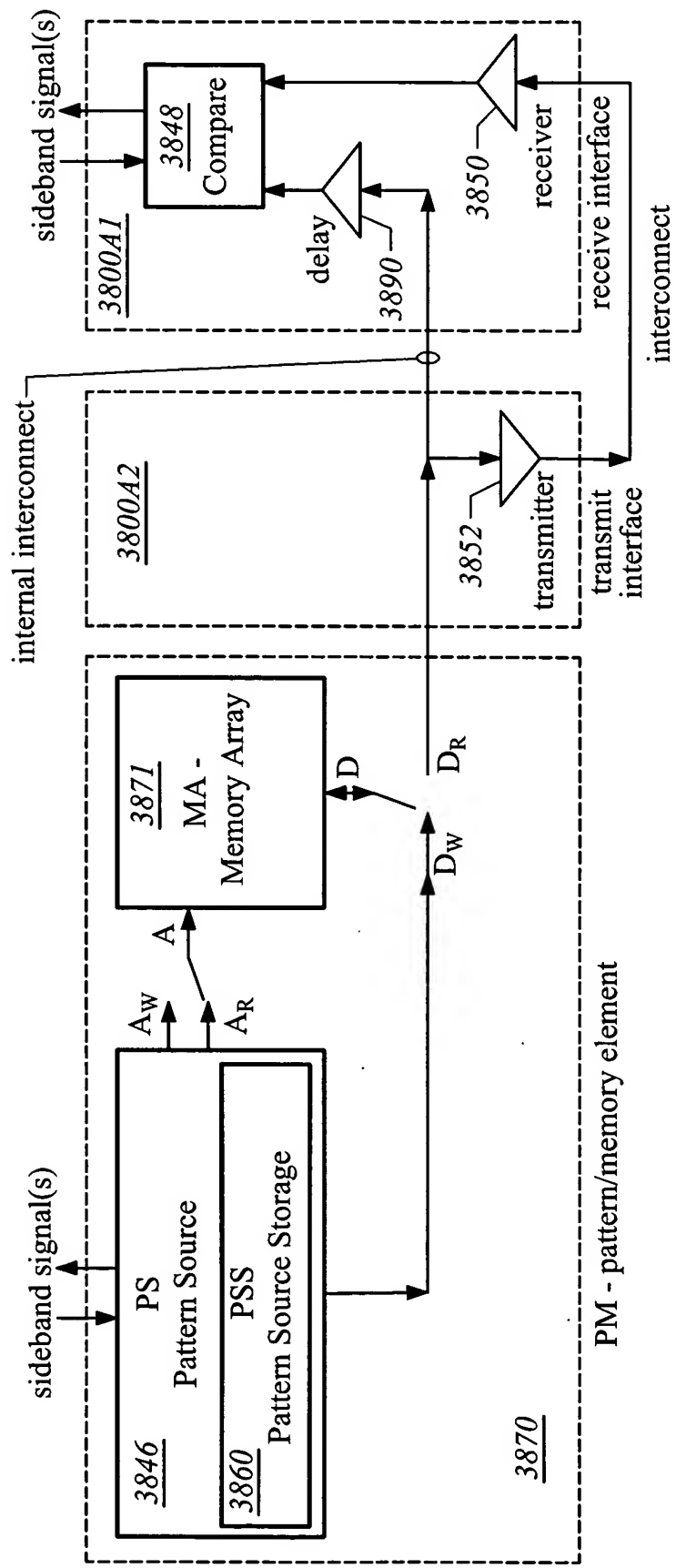


FIG. 38

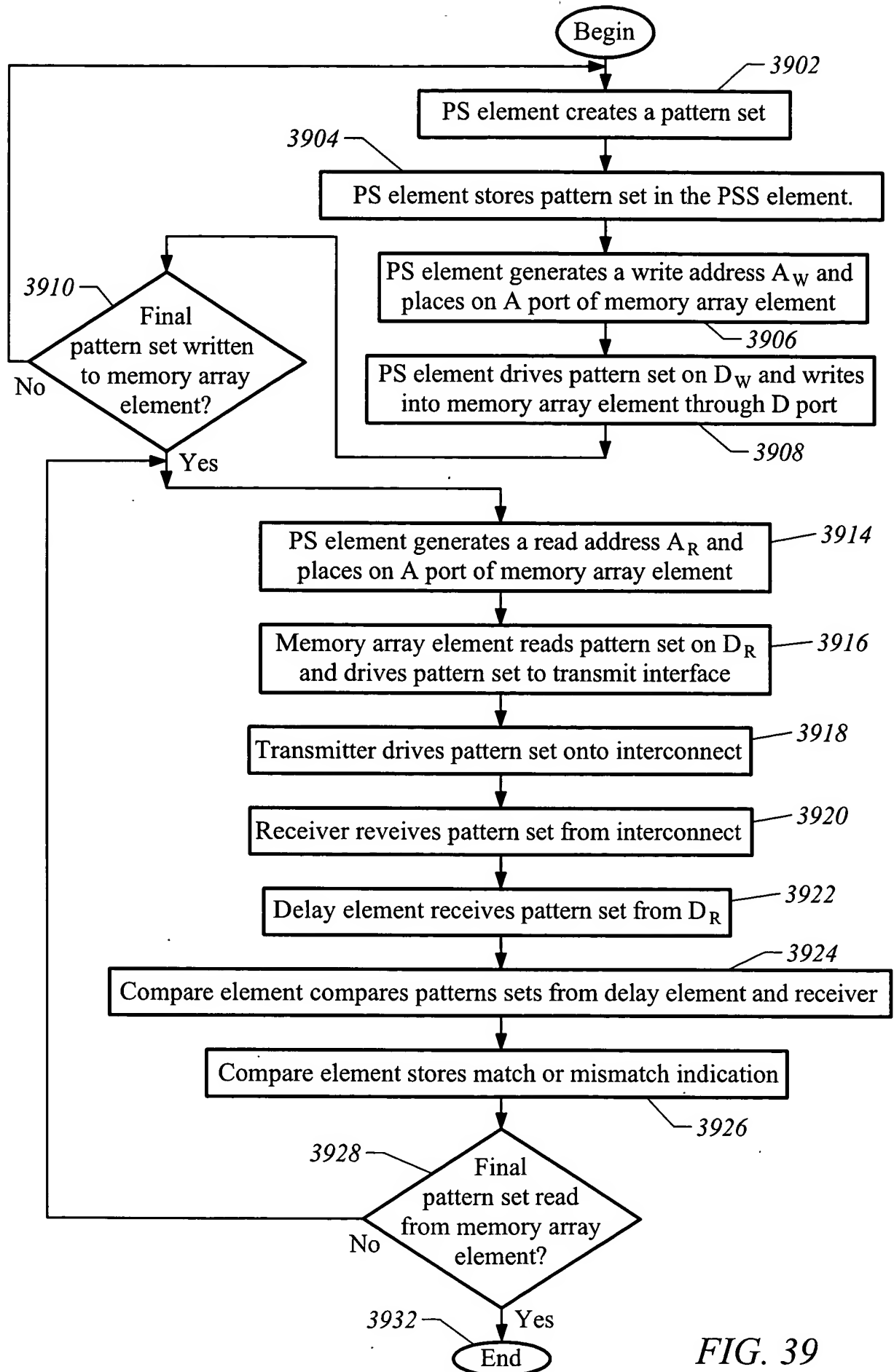


FIG. 39

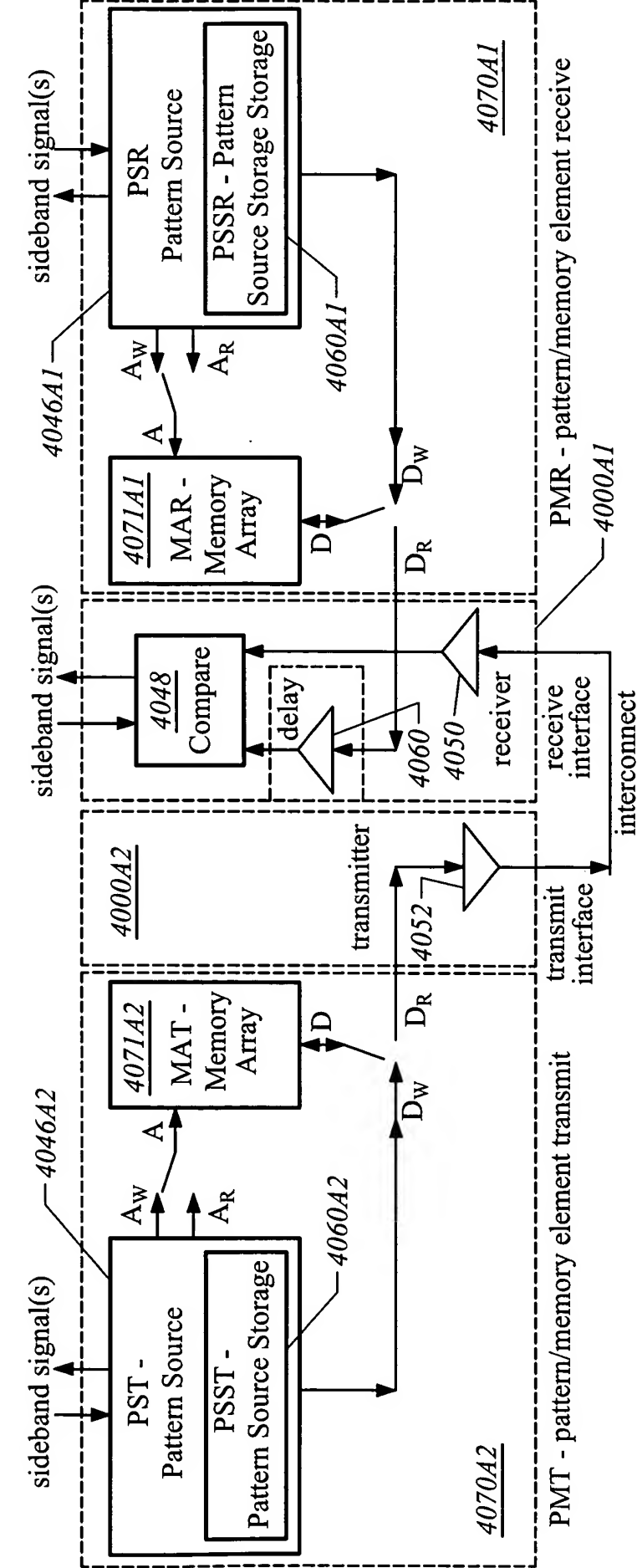
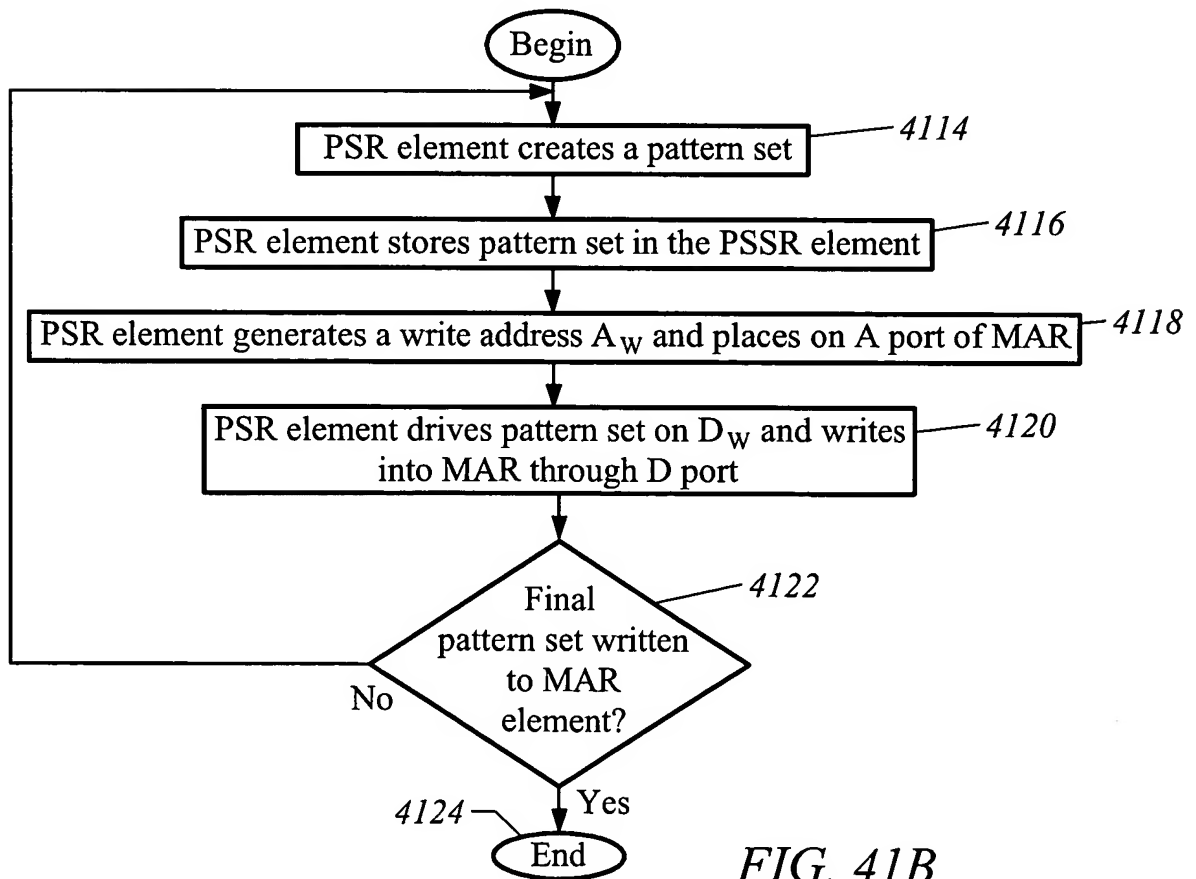
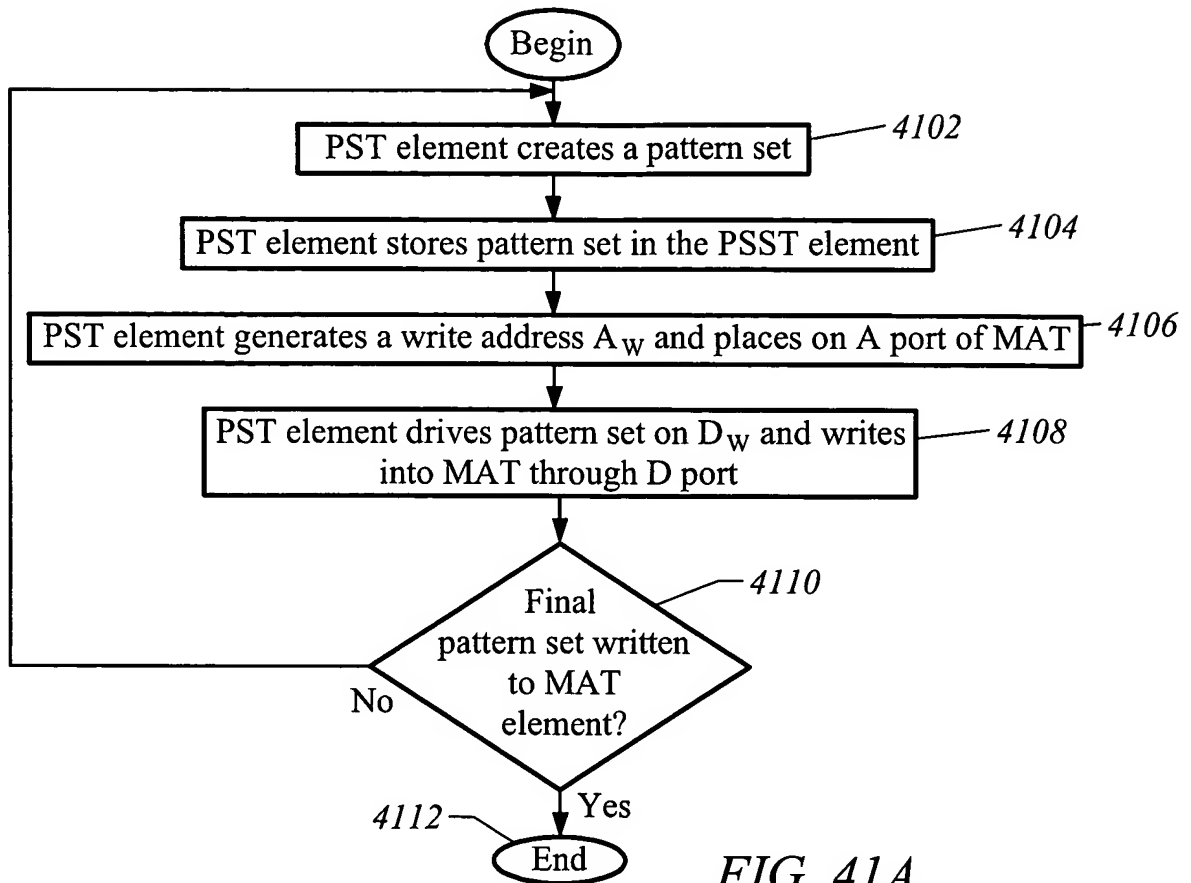


FIG. 40



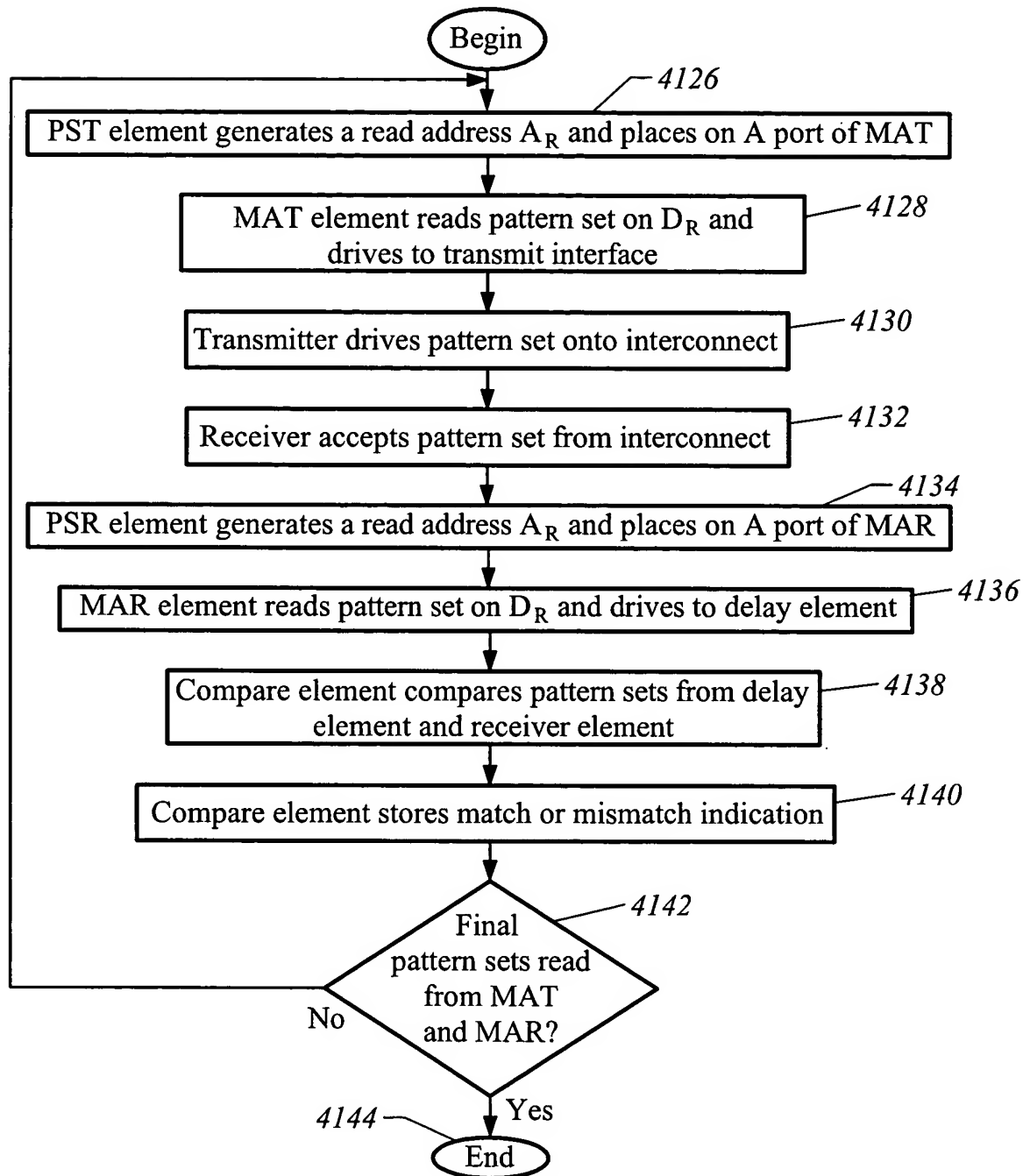


FIG. 41C

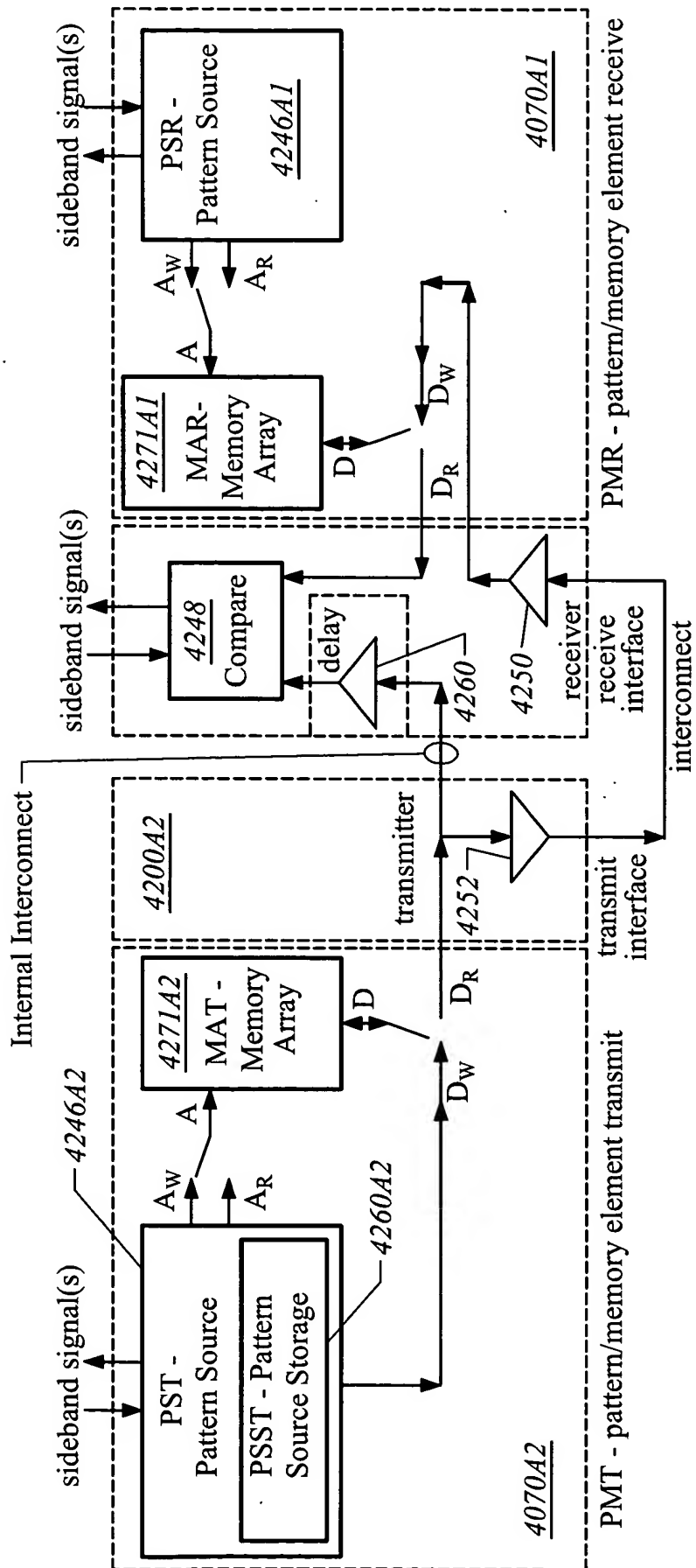


FIG. 42



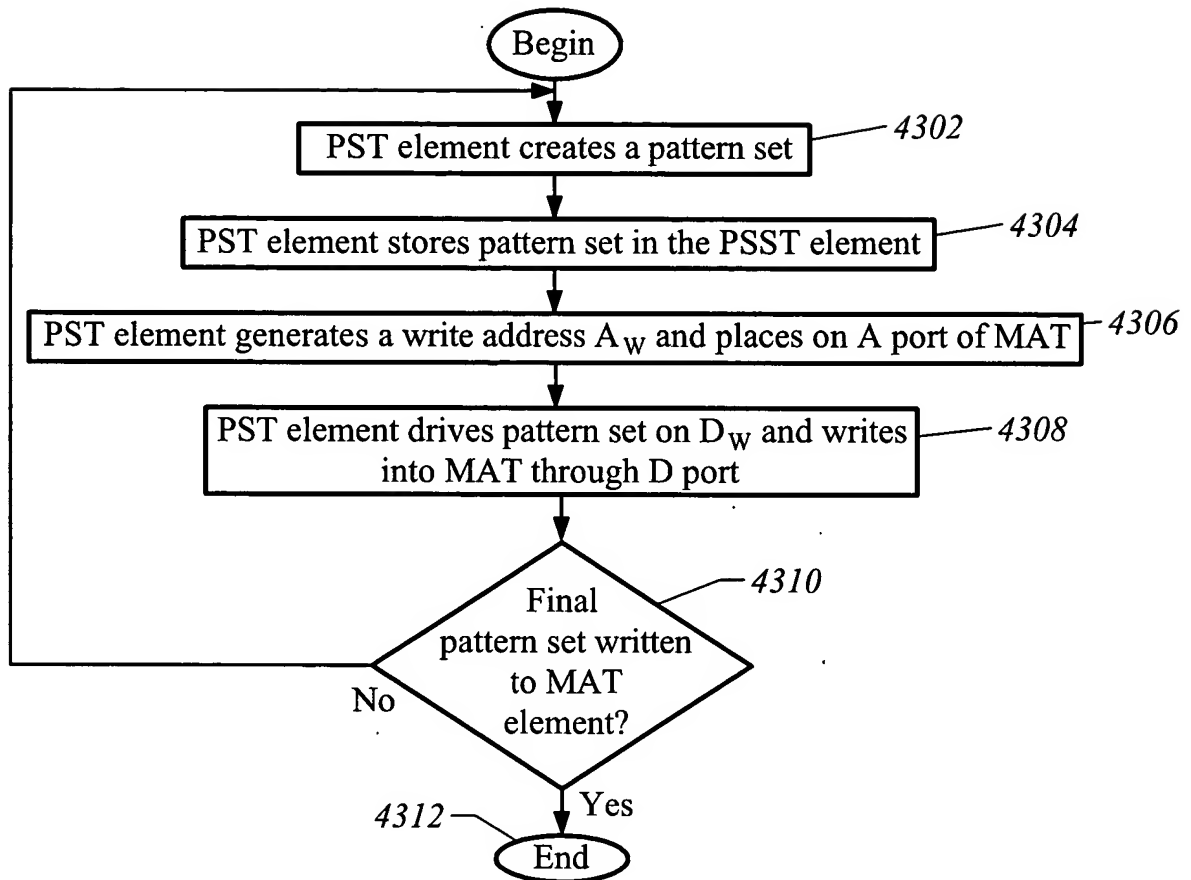


FIG. 43A

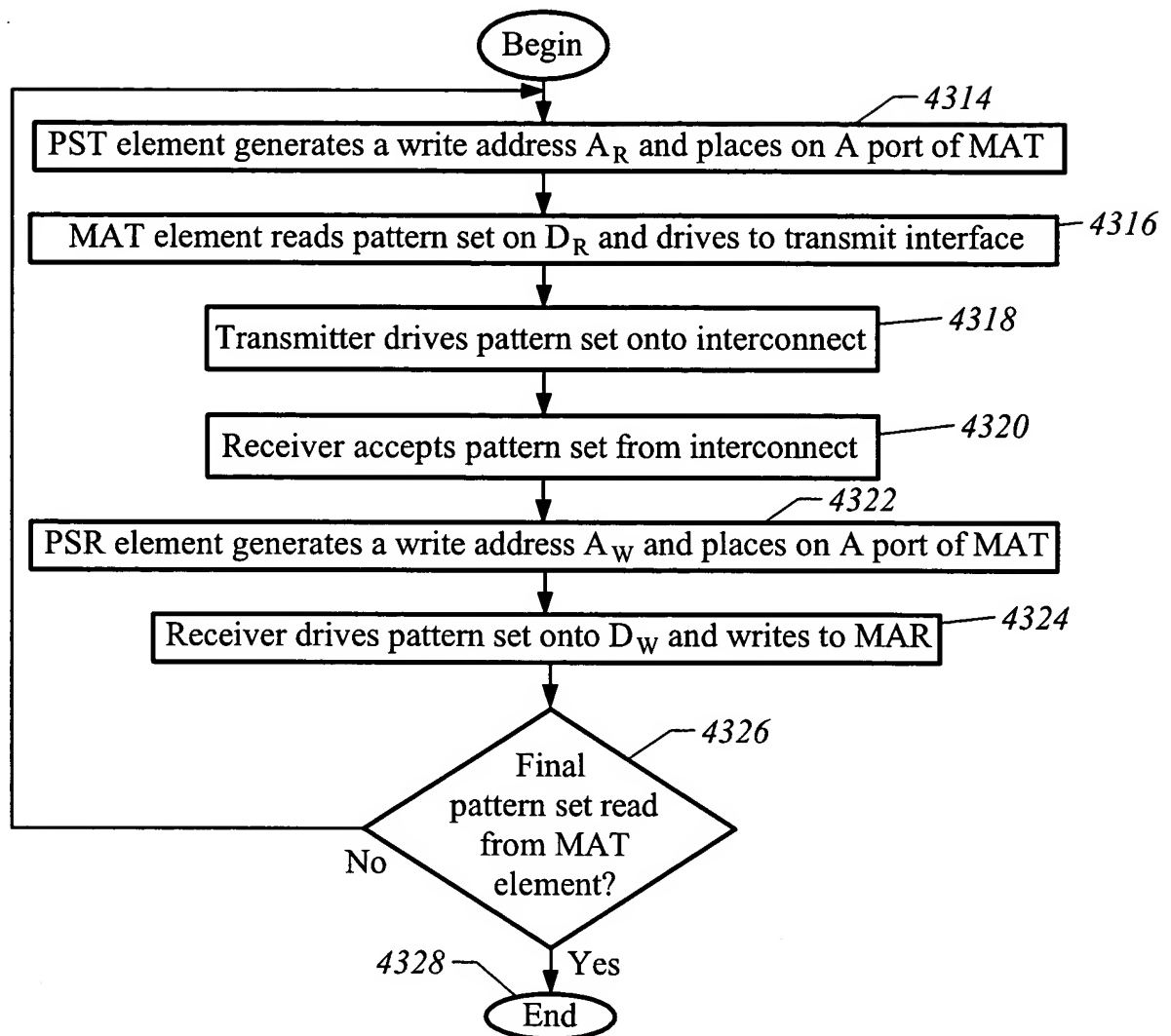


FIG. 43B

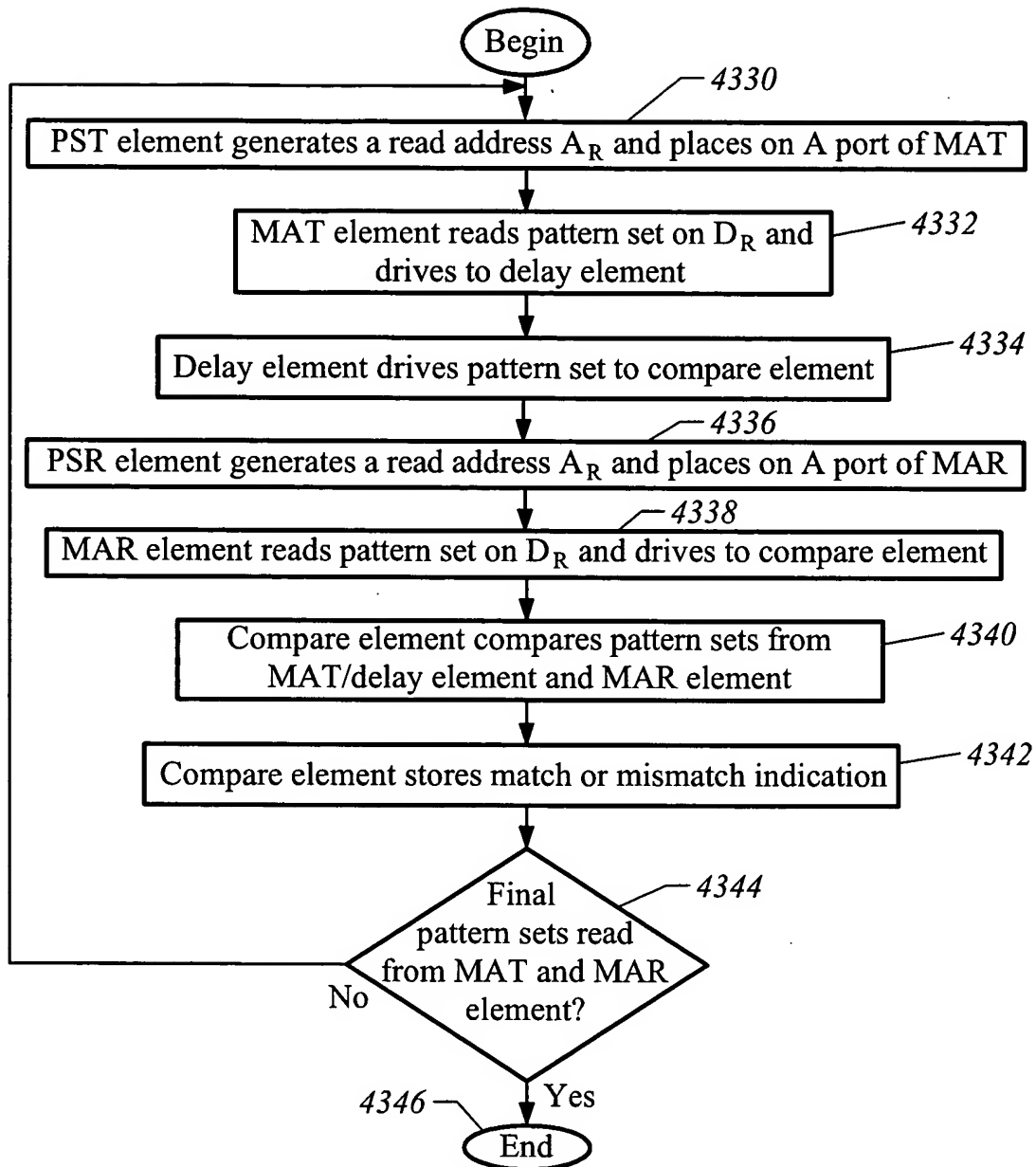
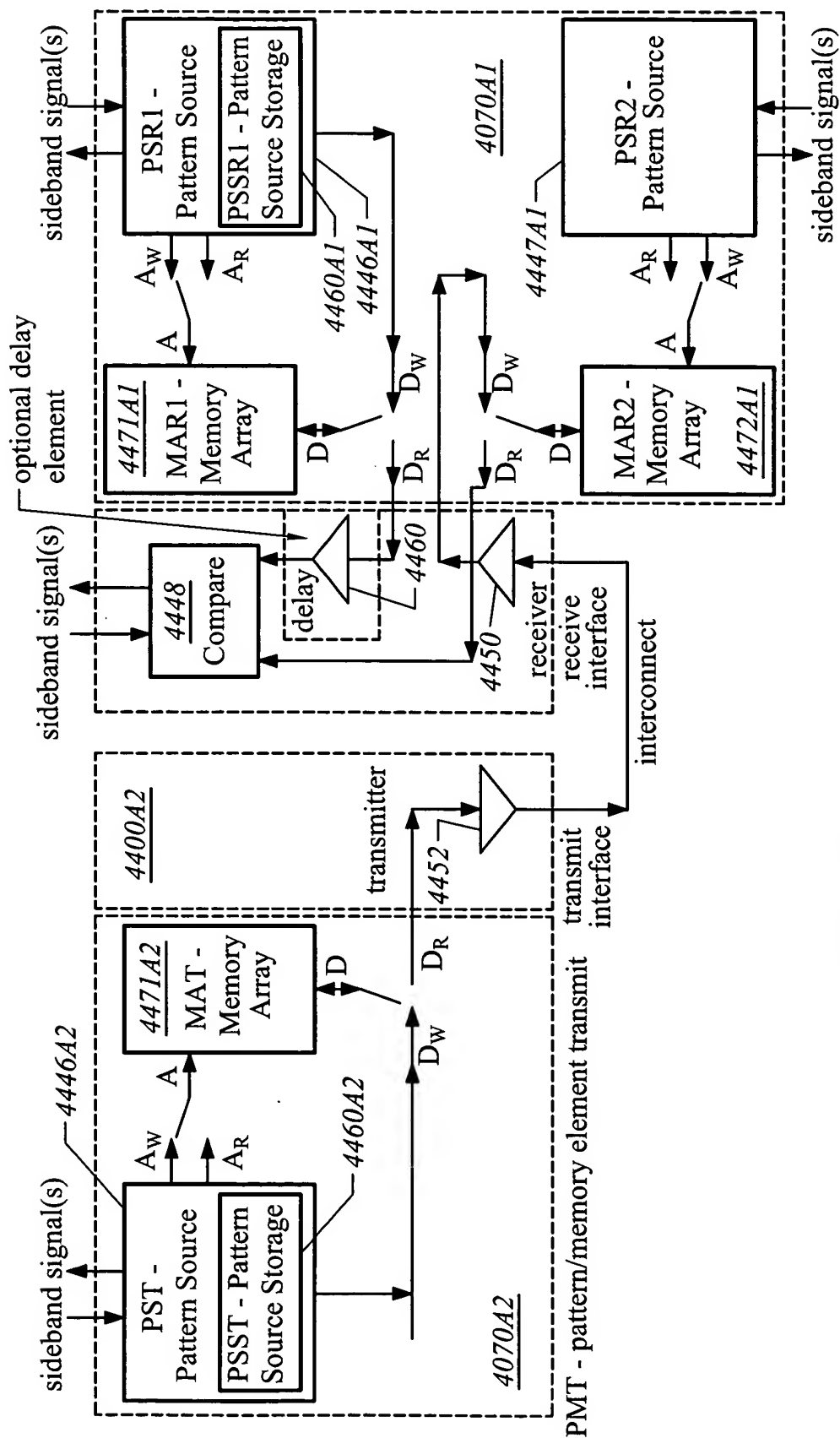
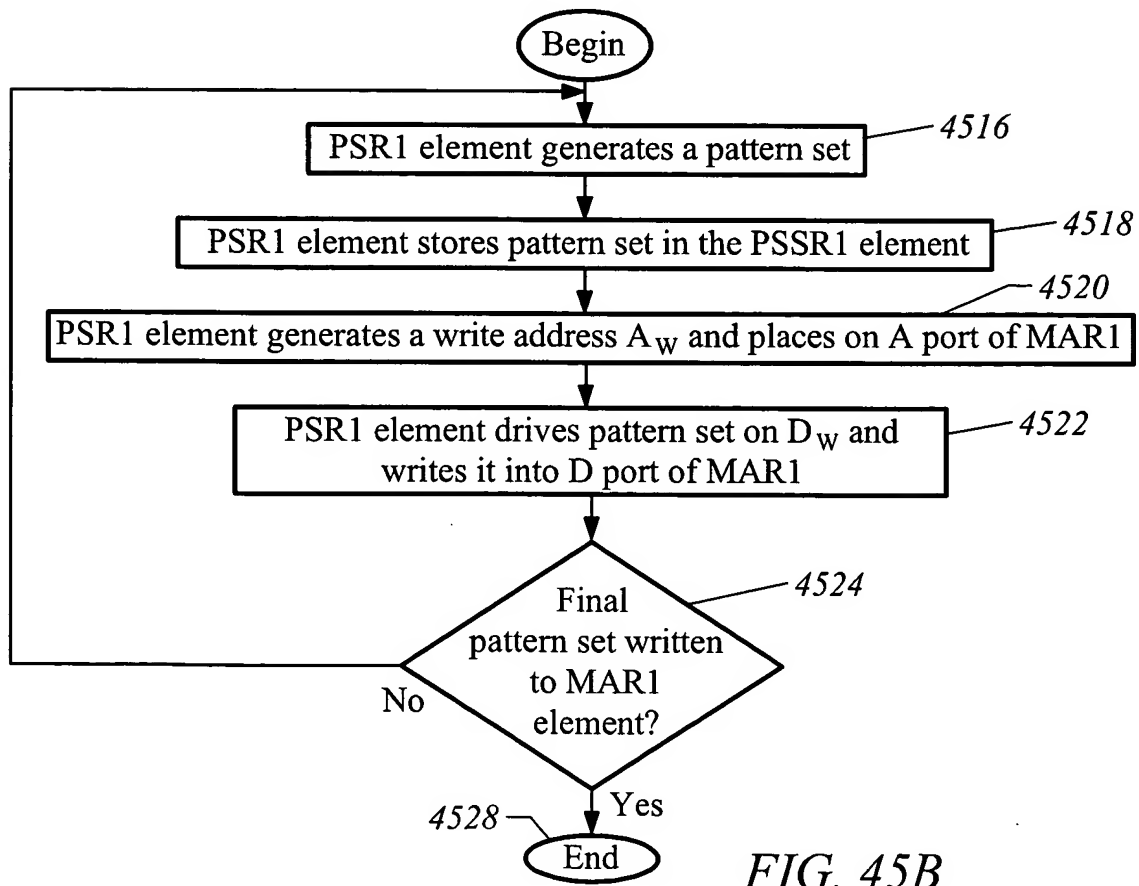
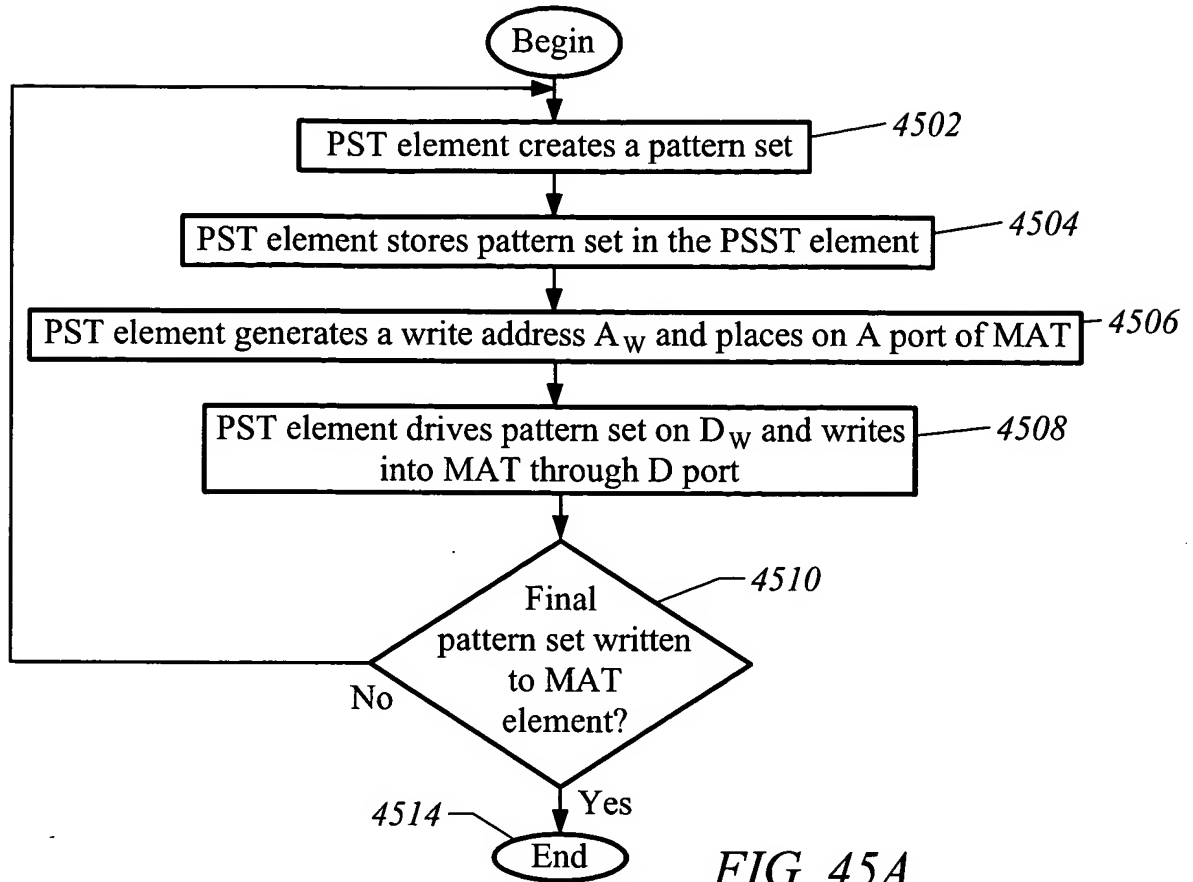


FIG. 43C



PMR - pattern/memory element receive



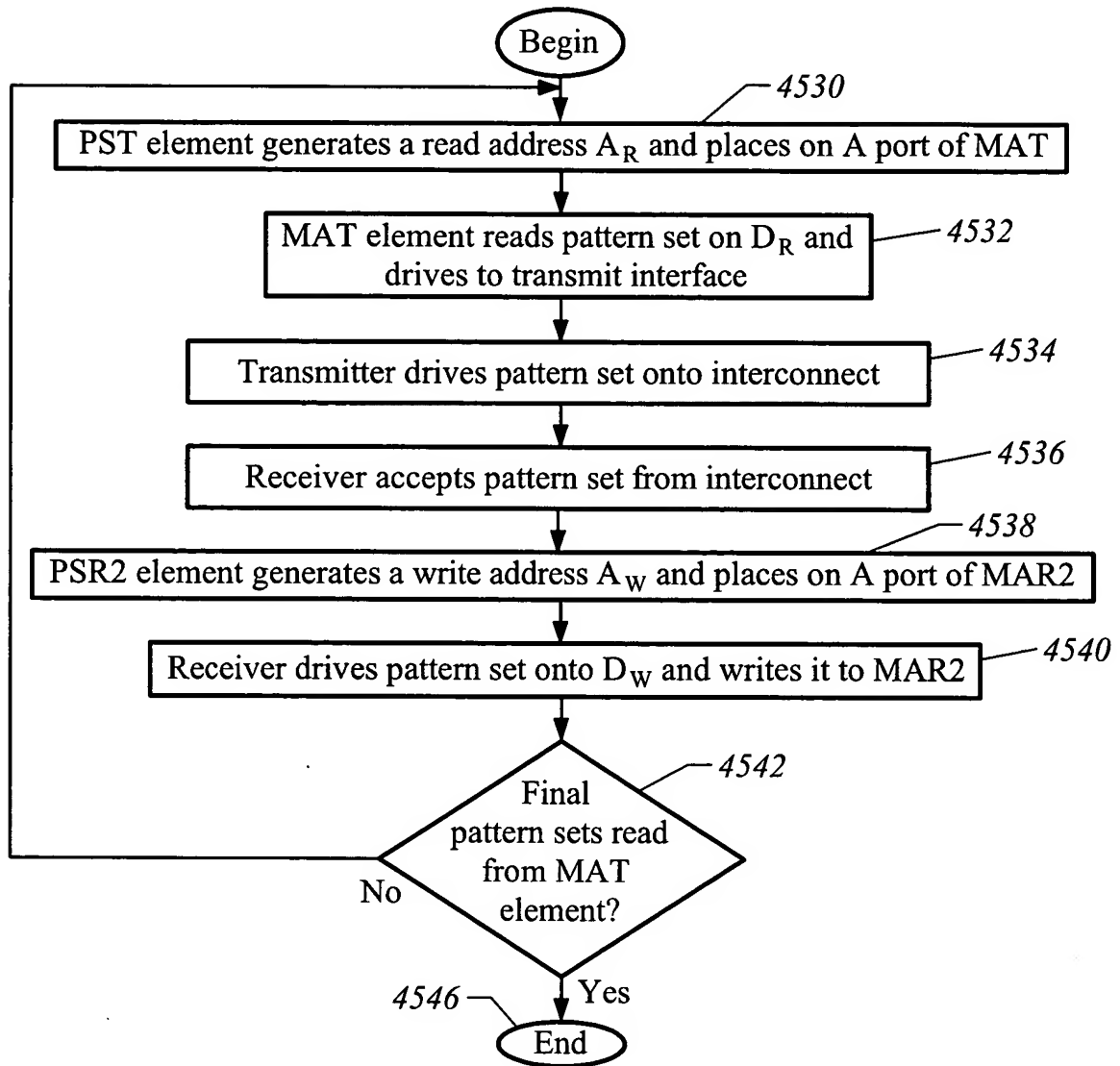


FIG. 45C

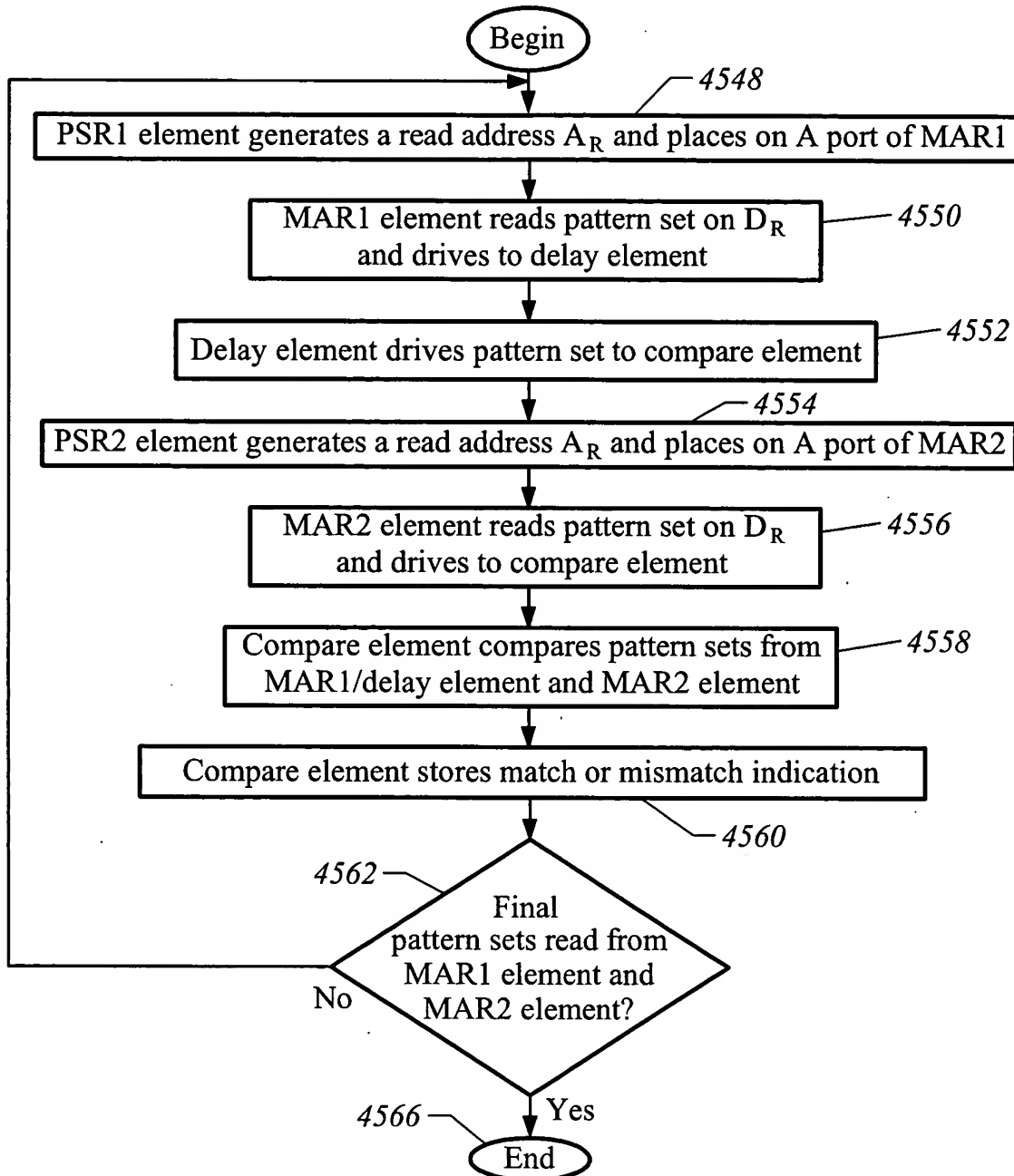


FIG. 45D

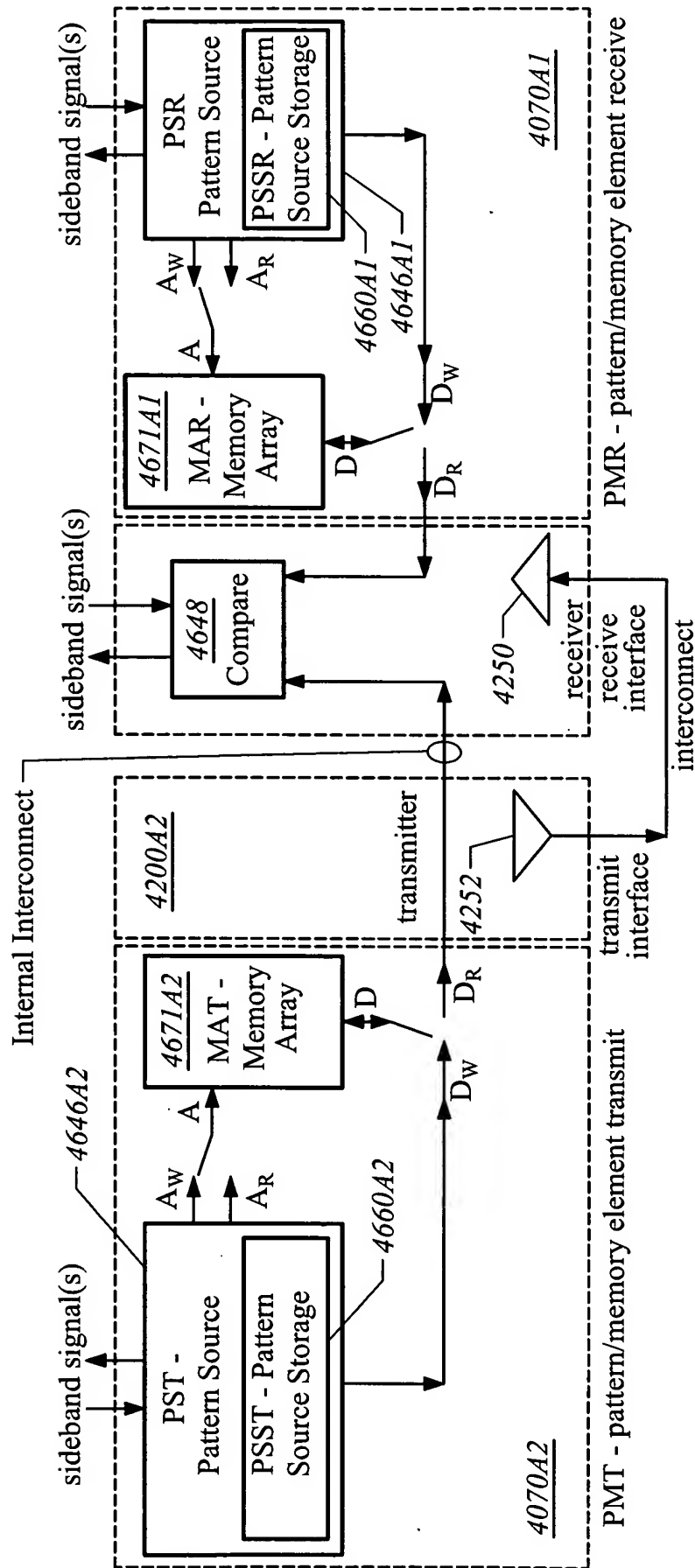
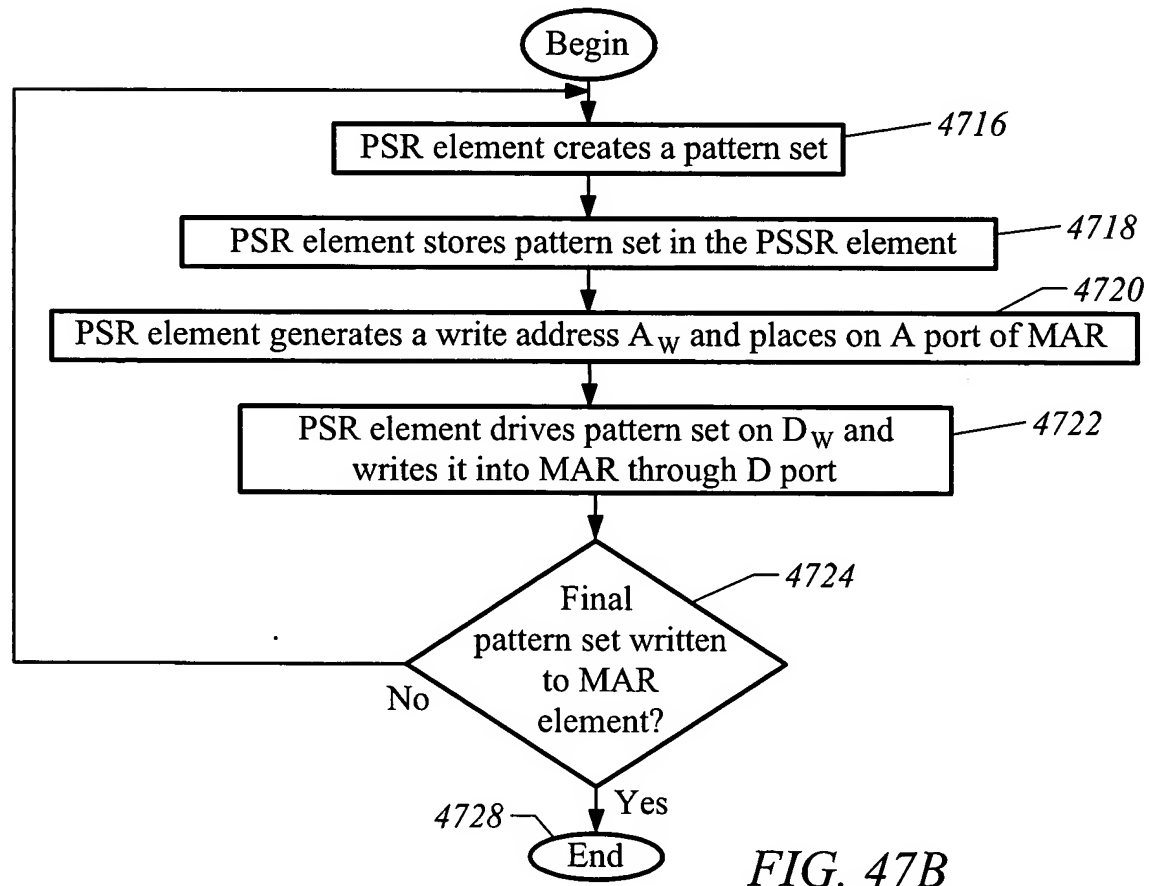
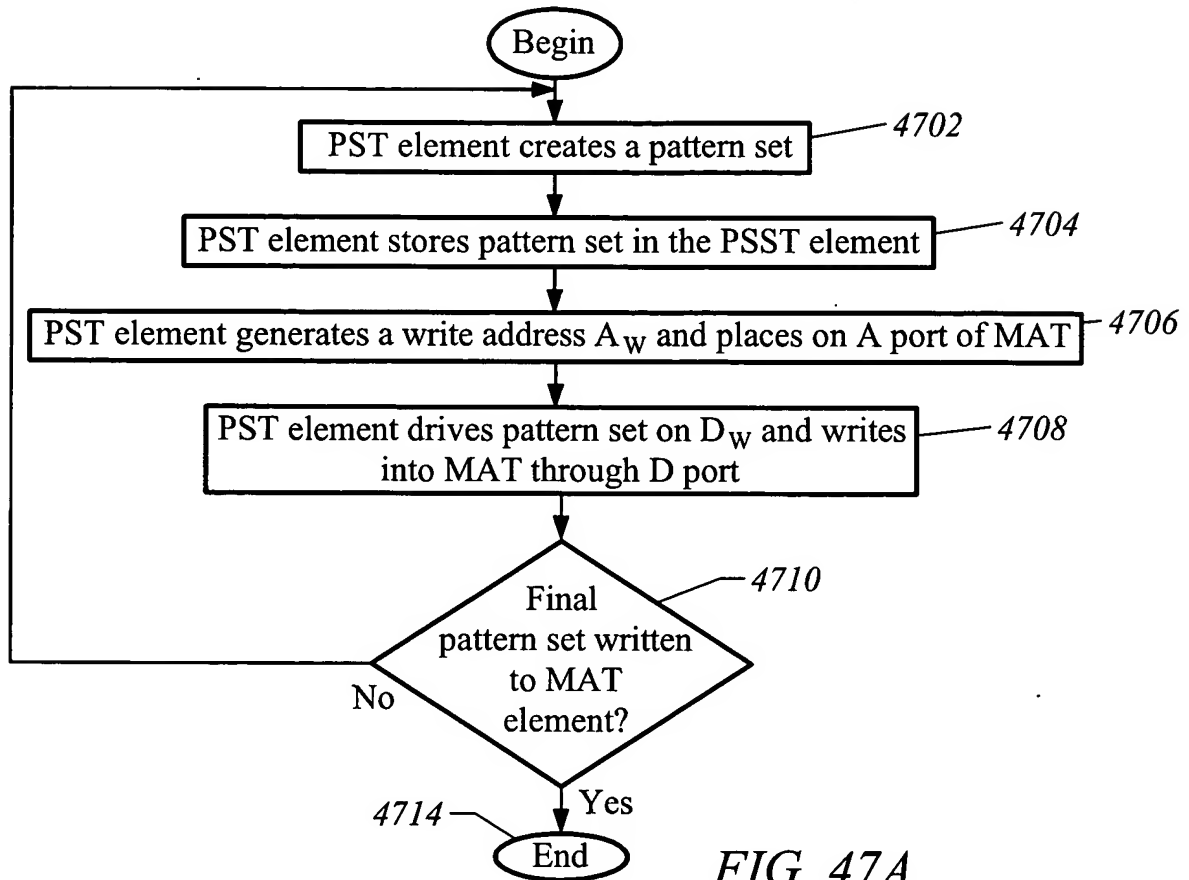


FIG. 46





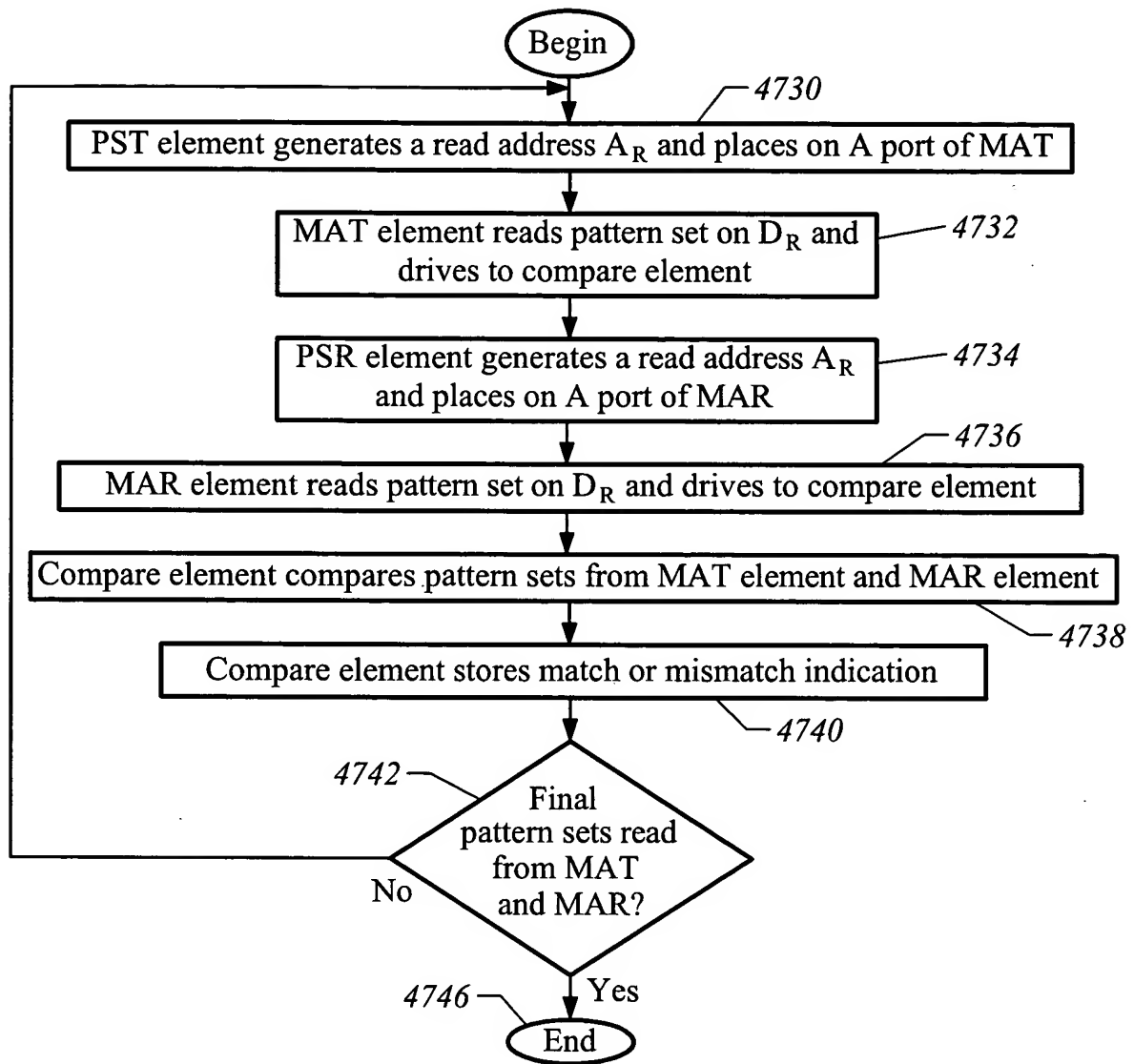


FIG. 47C